2007-05-17 Mike Perrott, MIT, "Making Better Use of Time in Mixed-Signal Circuits"

2007-05-14 Review Meeting

2007-05-03 Friedel Gerfers, Intel

2007-04-26 Henry Chang, Designer's Guide Consulting, "Verification of Complex Analog Integrated Circuits"

2007-04-19 Peter Kinget, Columbia University, "Designing Analog and RF circuits for Ultra-low Supply Voltages"

2007-04-12 Maryam Fathi, Stanford, "Challenges in Designing CMOS RF Power Amplifiers"

2007-03-15 Noam Dolev, Stanford, "Design Challenges of 10Gb Ethernet over Copper (Physical Layer)"

2007-03-01 Craig Levin, Stanford School of Medicine, "New 511 keV photon detector technologies for positron emission tomography"

2007-02-15 Boris Murmann, "My Favorite Papers of ISSCC 2007"

2007-02-01 Doug Mercer, Analog Devices, "Low Power Approaches to High Speed CMOS Current Steering DACs"

2006-11-30 Stephen O'Driscoll, Stanford, "Adaptive ADC Design for Neuro-prosthetic Interfaces"

2006-11-06 Review Meeting

2006-11-02 Mar Hershenson, Sabio Labs, "Advances in Analog Design Automation Methods"

2006-10-19 Sandipan Bhanot, Knowlent Corporation, "Next Generation Analog Testbench Environment"

2006-08-30 Summer Seminar: Mark Horowitz, "Scaling, Power and the Future of CMOS"

2006-08-03 Summer Seminar: Dean Liu, Aeluros Inc., "A Framework for Designing Reusable Analog Circuits"

2006-07-06 Summer Seminar: Boris Murmann, "Analog Design in sub-100nm CMOS"