

Nebojša Stanić

CONTACT INFORMATION

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EDUCATION

Ph.D. Electrical Engineering December 2006.
Columbia University, New York, New York, USA
Dissertation: "Low Voltage RF Integrated LC Filters and Communication Circuits"

M.S. Electrical Engineering February 2003.
Columbia University, New York, New York, USA
GPA: 4.0/4.0

Dipl. Ing. Electrical Engineering September 1999.
School of Electrical Engineering, University of Belgrade, Belgrade, Serbia
Diploma Thesis: "Three-Phase Thyristor Rectifier Bridge", GPA: 9.59/10.00

EXPERIENCE

Graduate Research Assistant September 2001. - present
Department of Electrical Engineering, Columbia University, New York, New York, USA
Research Interests: Analog and RF integrated circuits, signal processing; focussed on design of RF integrated LC filters and communication circuits at low voltage supplies in CMOS technology;

- Implemented Q-enhanced narrow-band bandpass LC filter, centered at 6 GHz, in 0.25 μm CMOS.
- Implemented a 900 MHz low-noise amplifier and mixer with LO buffers, for direct conversion receiver, operating at 0.5 V, in 0.18 μm CMOS.
- Implemented a 2.4 GHz low-noise amplifier, an image reject LC filter, and two mixers with LO buffers, for a heterodyne receiver, operating at 0.5 V, in 90 nm CMOS.

Summer Intern June - September, years 2002, 2003, and 2004.
Communications Research Lab, Intel Corporation, Hillsboro, Oregon, USA
Designed an integrated LC filter for WLAN IEEE 802.11a standard receiver. Analyzed use of LC filters for elimination of undesired bands in UWB application.

Visiting Scholar October 1999. - February 2000.
Department of Electrical Engineering, San Diego State University, San Diego, California, USA
Implemented algorithm for design of digital polyphase filters in MATLAB. Analyzed digital signal processing blocks in a system for digital predistortion of power amplifiers, and digital filters in a computer modem.

Teaching Assistant Spring Semester, years 2004. and 2005.
Department of Electrical Engineering, Columbia University, New York, New York, USA
Led lab experiments for an introductory electronics course for 120 students. Graded papers and led lab for course on communication circuits for 35 students.

November 1997. - September 1999, and July 2000. - September 2001.
School of Electrical Engineering, University of Belgrade, Belgrade, Serbia
Led lab, held recitations and graded papers for two electronics courses. Held recitations for a course on design of printed circuit boards.

PUBLICATIONS

Nebojša Stanić, Ajay Balankutty, Peter Kinget, Yannis Tsvividis, "A 0.5V Receiver in 90nm CMOS for 2.4GHz Applications", to be presented at IEEE RFIC Symposium, Honolulu, HI, June 2007.

Nebojša Stanić, Peter Kinget, Yannis Tsvividis, "A 0.5V 900MHz CMOS Receiver Front End", IEEE Symposium on VLSI Circuits, Honolulu, HI, June 2006.

Shaorui Li, Nebojša Stanić, Yannis Tsvividis, “A VCF Loss Control Tuning Loop for Q-Enhanced LC Filters”, IEEE Transactions on Circuits and Systems II, vol. 53, no. 9, pp. 906-910, Sep. 2006.

Shaorui Li, Nebojša Stanić, Krishnamurthy Soumyanath, Yannis Tsvividis, “An Integrated 1.5V 6GHz Q-enhanced LC CMOS Filter with Automatic Quality Factor Tuning Using Conductance Reference”, IEEE RFIC Symposium, Long Beach, CA, June 2005.

- COMPUTER SKILLS
- Electrical circuits simulators: Spice, SpectreRF; ADS and Cadence environment.
 - Electromagnetic simulators: EMX, HFSS, Asitic, IE3D.
 - Languages: C, Perl, Matlab, Mathematica, HTML.
 - System administrator: Unix, Linux.

- ACTIVITIES
- IEEE member. Reviewed technical papers for IEEE journals.
 - Member of Columbia University chess team.