



COLUMBIA | ENGINEERING

The Fu Foundation School of Engineering and Applied Science

Fall 2013

***MS of Electrical
Engineering***

EE Careers A to Z

***An outlook of jobs and careers in Electrical
Engineering for informational purposes***

careers@ee.columbia.edu



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ABOUT THIS BOOK

This book has but put together for educational and informational purposes. Students should utilize this book to understand what organizations are looking for both in terms of roles/positions and skillsets. This book is not comprehensive and should only be used as a tool to guide students in their early career exploration. Students are encouraged to further research the job market using the roles outlined in this book as a source. It is suggested that students use this book to find roles that appeal to and interest them while assessing their current and past skillsets. Students should also look to alternative sources to understand hiring needs and trends. Additionally the EE Dept. cannot guarantee employment in the roles outlined. A suggested best practice would be to find several roles of interest and look for similar roles across industry. From this, a student should then develop a targeted search ensuring their curriculum and coursework helps build vital skills while also developing a “tailored resume” amongst others job search tools as outlined in the MSEE Career Workbook.

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Concentration Area	Company Name	Sponsor - 2012 Count**	Sample Job Titles				
Computer Hardware	ASUS	Y - 14	Applications Engineer	Device Driver Engineer	Hardware Debug Engineer	Software Tools Engineer	Validation Engineer
	Seagate Technology	Y - 169	ASIC Development	Display Software Engineer	Hardware Design Engineer	SSD VLSI Design Engineer	
	Nvidia	Y - 117	ASIC Engineer	Electrical Engineer	Memory Validation Engineer	Storage Debug Engineer	
	Lenovo	N	ASIC Hardware Design	Failure Analysis	Network Engineer	System Design Engineer	
	OCZ Technology	TBD	ASIC Verification Engineer	Failure Analysis Engineer	Optical Design Engineer	System Software Engineer	
	Supermicro	Y - 11	BIOS Engineer	Firmware Developer	PCB Layout Engineer	Systems Engineer	
	Western Digital	Y - 155	Cloud Modeling and Data Analytics Engineer	Firmware Engineer	Software Developer	Systems Validation Engineer	
Computer Software/Software Engineering	Cadence Design Systems	Y - 48	Application Support Engineer	SSV Timing and Power Sign-off Intern			
	vmware	Y - 3	Applications Engineer	Technical Support Engineer			
	The MathWorks	Y - 10	Build Engineer				
	Informatica	N	Cloud Test Developer				
	Adobe	TBD	Kernel Engineer				
	Autodesk	Y - 3	Linux Networking Engineer				
	Symantec	Y - 3	Media Engineer				
	Synopsys	Y - 161	Physical Optimization Engineer				
	Dropbox	TBD	Software Engineer				
	SuccessFactors	TBD	Software Engineer - Tools				
Wind River	TBD	Software Security Engineer					
Electrical/Electronic Manufacturing	Apple	Y - 300	Acoustics Design Engineer	Digital Signal Processing Engineer	Hardware Design Engineer	Software Quality Test Automation Engineer	Validation Engineer
	Agilent Technologies	Y - 29	Analog Electrical Engineer	Electrical Controls Engineer	Hardware Systems Integration Engineer	Software Test Engineer	
	Eaton Corporation	Y - 86	Analog/Mixed Signal Design Engineer	Electrical Design Engineer	Power Management Engineer	Systems and Power Integrity Engineer	
	General Electric	Y - 606	Audio Software Engineer	Electrical Engineer	Software Development Engineer	Systems Engineer	
	United Technologies	Y - 62	Design Engineer	Embedded Software Engineer	Software Engineer	Test Engineer	
Information Technology & Services	GLOBALFOUNDRIES	Y - 523	Hardware Developer				
	Verizon	Y - 6	Network Engineer				
	Oracle	Y - 140	Network Engineer				
	Cognizant Technology Solutions	TBD	Software Test Engineer				
Integrated Circuits & Systems	AMD	Y - 462	Analog Design Engineer	Design Engineer	Embedded Software Engineer	Mixed Signal Design	Test Engineer
	Analog Devices	Y - 100	Analog IC Design Engineer	Design Verification Engineer	Firmware Engineer	Platform Engineer	Wireless Design
	Broadcom	Y - 250	Applications Engineer	Digital Design Engineer	Hardware Development	Product Development	
	Marvell Semiconductor	Y - 583	ASIC Engineer	DSP Architect	IC Design Engineer	RTL Engineer	
	Texas Instruments	Y - 691	CPU Design Engineer	Electrical Circuit Design Engineer	IC Design Verification	Software Engineer	
Networking & Communications	Cisco Systems	Y - 156	ASIC Design Engineer	Hardware Engineer	Software Development Engineer		
	Juniper Networks	Y - 62	ASIC Verification Engineer	Network Operations Center Engineer	Software Engineer		
	QLogic	Y - 67	Design Engineer	Power Engineer	System Test Engineer		
	Motorola Mobility	Y - 118	Firmware Engineer	Power Management Engineer	Systems Engineer		
	Nokia	Y - 170	FPGA Engineer	RTL Design Engineer	Test Engineer		

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Acoustics Design Engineer

Job Summary

The Audio Design & Engineering team is looking for an Acoustics Design Engineer who will be responsible for the acoustic design on programs and development of mobile phone systems.

Key Qualifications

- The ideal candidate will have experience in the design, prototyping and development of consumer audio systems
- Experience in the mobile phone industry
- Experience shipping high volume products is a must!
- Excellent knowledge of modern mobile phone architecture
- HATS setup, testing, and analysis
- Strong testing and data analysis skills
- Strong individual contributor
- Experience with:
 - Rhode & Schwarz UPL-16 audio analyzer
 - B&K acoustic transducers, especially artificial mouth and ear simulator
 - Acoustic anechoic chamber specification and usage

Description

As a Lead Acoustics Design Engineer, you will have responsibilities spanning all aspects of acoustics HW design: Transducer design, selection, and tuning with close interaction with the component suppliers Acoustic enclosure co-design Passive and active filter design DSP design and tuning for voicing, echo cancellation, noise suppression, side tone, etc. Design of products that use microphones, headphones/earphones, and speakers for personal audio systems Design and development of testing equipment and methods Manufacturing line bring-up, component validation, and manufacturing support

Analog Design Engineer

This engineer must be capable of hand (i.e. pen and paper) analysis and experience with the use of software tools such as Cadence Virtuoso, AMS, SPICE, etc. is required. This person must also understand basic function implementation and approaches for design optimization, transistor sizing and biasing tradeoffs, & low power optimization. Strong analog layout knowledge and parasitic component understanding are essential.

Other responsibilities include:

- In the planning phase, this engineer must be able to quickly understand functionality of existing circuit solutions and identify/propose unique, high value solutions, make decisions on technical and schedule tradeoffs.
- Top level verification of integrated circuit in a system environment.
- Design and optimization of analog functions including amplifiers, comparators, bandgap references, IO buffers, precision data converters, etc.
- Understanding IC fabrication processes and translating process capabilities and system level requirements into real world designs.
- Proficient use of oscilloscopes, arbitrary waveform generators, and measurement devices for laboratory evaluation of new silicon and competitive analysis

Basic Qualifications

- * Analog circuit design experience with amplifiers, comparators, bandgap references, IO buffers, precision data converters.
- * Experience using Cadence Virtuoso and Spice
- * Lab equipment proficiency is necessary.
- * Fluent in verbal and written English.
- * Experience in working with US or Europe design team is a plus

Job Title: **Analog Design Engineer I**

Qualifications:

- Masters/Bachelor's Degree in Electronics/Electrical Engineering from a reputable university with 1-2 years' experience
- Experience in IC design in one of the following areas: PA, LNA, Mixer, Gm-C filter, ADC, DAC and mixed-signal design
- Experience in Cadence design tools such as Analog Artist, SpectreRF, Goldengate) is required
- Knowledge of communication standards such as GSM, EDGE, UMTS, LTE and mixed-signal components
- Familiar with RF test equipment including VSA, network analyzer etc.
- Fresh Graduates with a good academic record are also welcome to apply

Description:

- Design state-of-art analog/RF integrated circuit building blocks in advanced CMOS process technologies
- Perform correlation and silicon evaluation in laboratory

Profession: Engineering - Hardware

Discipline: Analog IC Design

Analog Electrical Engineer

United Technologies Aerospace Systems, ISR Systems division, has over 50 years of experience in designing, building and testing exceptionally high quality optics and electro-optical systems. The Danbury facility (formerly Goodrich, Perkin-Elmer, Hughes and Raytheon) delivers these high quality systems to customers such as NASA, Department of Defense and commercial customers. The highly skilled team at Danbury produces unique electro-optical payload systems for space as well as special purpose payloads for ground and airborne systems. We need you to become part of this team to deliver these complex systems that explore space, plot the heavens and protect our armed forces. Come join us today!

We are seeking candidates for an Electrical Engineering Analog design position. Candidate will have the opportunity to work on state of the art Military and Space hardware product lines. The primary roles and responsibilities of this position are to provide analog design solutions and product line support for varied applications including complex motion control applications, earth sensors, star trackers, reaction wheels, magnetometers, torque rods, laser warning systems and CCD based instruments.

Design experience should include strong a knowledge base related to the design and test of high reliability electro-optical systems. Candidate should demonstrate strong analog design skills and be able to assume lead work on electrical engineering tasks role as related to both the support of legacy products and development of new designs in applications ranging from custom power supply design, focal plane technologies, motor drive design, position sensing & control, temperature sensing & control, low noise preamplifier design and telemetry systems.

Additional job duties include end of life performance validation through Worst-Case Analysis, signal integrity analysis as related to transmission lines & high bandwidth systems and support of both internal and customer related design reviews. Familiarity with engineering design tools such as PSPICE, Hyperlynx, MATLAB & MCAD is desirable.

- Design, Analysis, and Test of analog electronic systems.
 - Develop engineering specifications which require Military standard requirements.
 - System Level Acceptance Testing Engineering Support: TVAC, Vib, EMI, etc.
1. Bachelor's degree in Electrical Engineering required. Master's degree preferred.
 2. 0 to 3 years of electrical engineering experience.
 3. Less than 10% travel required.
 4. US Citizenship required in support of security clearance requirements.

Education: Bachelor's degree in Electrical Engineering required. Master's degree preferred.

Yrs. of Experience: 0 to 3 years electrical engineering experience.

Candidate must be a US Citizen and must either hold a current

US DoD clearance or must be able to obtain and maintain a clearance.

Analog/Mixed-Signal Designer

Career Level

Entry-Level

Function

Engineering/Technology

Essential Responsibilities

You will:

- Design complex, multi-channel, high-performance, mixed-signal electronic devices as part of a multi-disciplinary team and help ensure their transition into GE products
- Be responsible for all components of the ASIC development cycle: architecture design, process selection, circuit design, simulation, checking, layout and testing.
- Contribute to vital tasks on technology programs and assist in transferring technology to GE businesses, vendors, and strategic partners.

Effectively communicate results by preparing written reports and making presentations describing analyses performed, solutions developed, and value obtained

Qualifications/Requirements

- Doctorate in Electrical Engineering or Physics with a concentration in Analog IC design
- Proven design and analysis skills in the area of A/D and D/A conversion and signal conditioning
- First-hand experience of a complete mixed-signal ASIC design that was fabricated and tested
- Experience with low noise and low power design
- Proficiency in CMOS/BiCMOS designs
- Proficiency in MATLAB and C programming languages
- Proficiency in circuit analysis using modeling software (SPICE, SABER, ELDO, SPECTRE).
- Must be willing to work out of an office located in Niskayuna, NY.

Must be willing to take a drug test and submit to a background investigation as part of the selection process.

Desired Characteristics

- Familiarity with deep submicron design
- Knowledge of VHDL and Verilog digital hardware description languages
- Exposure to medical, sensor, high temperature, harsh environment, or novel semiconductor electronics.

Analog Test engineer

Job Description Experienced Analog IC Test Engineer for Computing Power Management

Role Summary
Develop the analog mixed signal IC test solution for computing power management.

Primary Responsibility

- Develop test hardware and software on Automated Test Equipment (ATE)
- Define and implement test procedures for new product verification, qualification, characterization, and production
- Drive for quality through test coverage
- Develop strategy for a better test methodology
- Test data collection and analysis
- Test lab equipment operation and maintenance

Secondary responsibility

- Interaction with Design, Systems, and Application engineer during the product development.
- Responsible for the product development.
- Drive and manage the product development.

Language skill needs to be native Mandarin for an applicant who has interest in applying for this position.

Antenna Engineer

Basic	Masters degree
Qualifications	Must have classes or coursework in antenna or rf engineering
Department Description	The Advanced Systems Development (ASD) team creates business growth opportunities for the division by working closely with the end customers in creating new higher risk differentiated solutions that are typically outside of the current divisional commitments.

Scope of Responsibilities/Expectations

- Provide antenna designs for new product developments.
- Provide antenna designs for new technology developments.
- Validate design concepts with disciplined testing, analysis and simulations.
- Stay current with emerging technologies.
- Support the development of prototypes & technology demonstrators.
- Work as part of a larger team in defining product requirements.
- Visit and work with customers to provide innovative product solutions.
- Work with and manage outside design firms & consultants.
- Work with cross-functional teams on non-electrical system disciplines, i.e., mechanical integration, industrial design, etc.
- Provide technical presentations.
- Write proposals & patents.
- Create development schedules, including risk mitigation plans.

Specific Knowledge/Skills

Minimum of a Masters degree in Electrical Engineering required , PHD preferred. Engineering experience in antenna design and development with a strong RF background.

- Experience in antenna design and analysis as applied to highly integrated handheld mobile computers or infrastructure based larger systems/devices.
- Experienced with antenna designs as applied to WAN, LAN, PAN, GPS, & RFID networks.
- Experienced with EM Simulation tools: CST, xFDTD, HFSS
- Experienced with performing chamber measurements, postprocessing and analyzing data. Satimo chamber experience preferred
- Familiar with basic circuit design & schematic development.
- Knowledgeable in digital & analog circuits.
- Schematic entry using CAD packages such as Cadence or OrCad.
- Testing and troubleshooting experience using basic equipment such as vector network analyzers, spectrum analyzers, GTEM cells, oscilloscopes & DMMs.
- Familiar with interconnection design including flex & cable.
- Knowledgeable in system integration including; ESD, and EMI design.
- Excellent communication skills including both verbal and written.
- Small project management and organizational skills are desirable.
- Experienced at multi-tasking several projects simultaneously

Application Support Engineer

As an Application Support Engineer Intern you will become an expert in MathWorks products by supporting our customers and providing solutions for highly complex technical issues. You will also provide valuable feedback to our development teams and become a resource for our worldwide team. You will receive a combination of practical experience and formal training in a work environment that fosters and rewards cooperation, teamwork, learning, and fun. This will allow you to gain:

- Product knowledge.
- Troubleshooting skills.
- Software testing and debugging.
- Exposure to real-world applications.
- Opportunities to develop your communication, presentation and leadership skills.

You will be working closely with full time Application Support Engineers and have the opportunity for consideration to transition to a fulltime Application Support Engineer in EDG and other departments in MathWorks.

Qualifications Required / Preferred

- MS or PhD in Engineering (i.e. Electrical or Mechanical) or Computer Science
- Strong programming skills
- Excellent communication and time management skills are a must

Strong Math/Calculus background
MATLAB knowledge or experience is a plus

Applications Engineer – Validation

Qualifications:

- Prefer MSEE with experience in SERDES products, SAS, SATA, USB, PCIe.
- Ability to make technical presentations, application notes & quickly get knowledgeable with company products.
- Need to have a strong technical knowledge of disk drive or SSD products. SOC link layer experience a strong plus.
- Uses professional concepts and company's policies & procedures to solve a wide range of difficult problems in practical ways. Works on problems of diverse scope. Exercises judgment with generally defined practices and policies.
- Familiar with high speed circuit architectures in phase lock loop (PLL), clock data recovery (CDR), feed forward equalizer (FFE), decision feedback equalizer (DFE).
- SAS 12G experience in transmitter-receiver training (both PHY level and protocol level) are strong plus.

Description:

- Responsible for winning designs and managing accounts on a technical level.
- PHY candidate: Evaluate and debug new features in PHY, assist customers in the development of drive firmware, collect performance data, and resolve application/production issues. These includes SAS 12G and PCIe Gen 3 products.
- Provides technical support to customers including customer visits and field design support.
- Acts as a primary technical interface between the company and the customer.
- Establishes Goals and Objectives with the sales staff and representatives of each target account. Qualifies design activity at the customer engineering level.
- Participates in the definition of new products and the identification of niche areas that present new opportunities for Marvell.
- Makes new product recommendations to Design/Marketing.

Profession: Engineering - Hardware

Discipline: Field Application Engineering

Business Title **Applications Engineer**

Job Category

Hire Type

Job Description
and
Requirements

Engineering

Employee

Corporate Applications Engineer (CAE) for the DFTMAX product is responsible for providing technical support to field engineers, technicians, and product support who are diagnosing, troubleshooting, and debugging the DFTMAX tools. Responds to situations where first-line product support has failed to isolate or fix problems in malfunctioning software. Major tasks involving product specs and/or features with high complexity. Involved in new and existing product evolution. May be involved in customer installation and training. The individual will have regular and proactive contact with external customers to deliver customer specific training or minor consulting services. Will also interact with other internal contacts (i.e. marketing, product development R&D, sales). Works on key customer accounts and may work across product teams with other products such as PrimeTime, VCS, DC, and ICC. Writes and reviews methodology specifications, functional specs, support plans/test plans. Must have good problem solving skills, good communications skills (written, oral, and presentations). Some occasional travel may be required.

The ideal candidate has a basic overall understanding of the design process, is proficient with C/C++, UNIX, HDL (Verilog/VHDL), and has a strong understanding of ASIC design flow, VLSI, and/or CAD engineering.

Knowledge of competitive EDA tool products and product knowledge in any of the areas of Synthesis, Simulation, Verification, Place and Route, Design Reuse and/or Physical Design is preferred.

BS/MS Electrical Engineering, Computer Engineering. No previous work experience required.

ASIC Design Engineer

QLogic, a leading supplier of high performance storage networking solutions, simplifies the process of networking storage by providing the only end-to-end SAN infrastructure in the industry. QLogic builds solutions based on all storage network technologies including Fibre Channel, iSCSI, and FCoE/Ethernet.

We currently have an exciting opening for an ASIC Design Engineer focusing on FPGAs. Job duties may include implementing RTL, simulation, lab test, and physical design of FPGAs to support our next generation of networking technology using industry standard tools and design methodologies. The position requires working with ASIC design, verification and firmware and driver teams to deliver high quality products to our customers.

Required Skills

- BS degree in Computer Engineering or related discipline
- 6+ years of experience or equivalent
- Experience designing ASIC hardware blocks in synthesizable (RTL) Verilog/System Verilog HDL Code
- Excellent data structure knowledge
- Excellent written & verbal communication skills.
- Problem solver with “get it done” attitude and a flexible skillset

Additional Skills

- Experience with the following design activities:
 - Synthesis, timing analysis for ASIC or FPGA
 - Partitioning and place and route for FPGA
 - Synchronization techniques for multi-clock domain ASICs
 - Debugging simulations
 - Silicon/FPGA debug
 - RTL Verification e.g. Constrained Random, Directed test cases, assertion based, formal algorithms
 - Scripting languages such as Perl or Python
- Experience with the following industry concepts:
 - Networking implementations and protocols, including Ethernet, IP, IPv6, TCP, iSCSI, FCoE and FC
 - Computing implementations and protocols, including PCIe
- Experience with lab equipment such as: logic, PCIe and network analyzers

ASIC Design For Test Engineer

Job Description: For years, Cisco's vision has been to change the way the world works, lives, plays, and learns. Our vision is more relevant today than ever. We made the Internet what it is today. First, we focused on creating connectivity. Now, we're entering the Internet of Everything transition—an era where we'll help create unprecedented value by connecting the unconnected. The Internet of Everything is a global industry phenomenon that is driving the biggest market transition for Cisco and our customers. This includes the intelligent connection of people, process, data, and things. It's where everything is converged on the Internet, making networked connections more relevant and valuable than before. To help us bring this vision to life, join us in our exciting journey.

Core Responsibilities:

- ASIC DFT engineer for top level architect, implement and verification of DFT features including MBIST, Logic BIST, SerdesBIST, IOBIST, JTAG, SCAN/ATPG, PLL test, and more.
- Write a detailed implementation and verification plan for the DFT functionality.
- Subchip level and Fullchip DFT simulation to ensure DFT logic will work.
- Build and run DFT regression to validate any ECO netlists.
- Work with Design team and ASIC vendors to ensure Cisco ASIC DFT requirements are met.
- Provide silicon bring-up support and debug.
- DFT IP development and deployment.

Qualifications:

- Proficient of Verilog and experience with simulators and waveform debugging tools
- Experience debugging gate-level simulation, both in 0-delay and SDF environments
- Knowledge of DFT Architecture, including ATPG, JTAG, MBIST & LBIST
- Knowledge of industry standard DFT and design tools is desired but not required. Tools such as Synopsys DFT Compiler, Tetramax, PrimeTime, VCS, ncsim, Encounter Test.
- Experience with one or more scripting languages: Perl, TCL, csh, awk, python.
- Comfortable editing/using Makefiles
- Ability to conduct experiments during silicon debug, gathering and analyzing data, and to utilize scripting to support efficient handling of ATE data.

ASIC Development

Job Description:

Develop, implement and debug embedded firmware to be used for validating SoC hardware in the functional areas of disk sector read/write, wedge read/write and other related process modes used for drive products. Disk sector read and write stress testing is key role in the SoC disk side validations. Expand the disk validations to include matrix silicon and voltage and temperature testing. Provide disk and channel usage case coverage and debug support for Applications Engineering, SoC Design, and Drive Firmware groups.

Job Requirements:

- Experienced with C embedded firmware
- Must operate in multi-site environment to coordinate and satisfy goals which have broad business impacts
- Good writing and communication skills
- GUI development for testing and data collection

Responsibilities:

- Develop and maintain disk validation stress tests leveraging existing validation firmware. Create additional firmware as needed to implement the tests.
- Develop and adapt tools for analysis and generation of input and output waveforms and signals, and code traces
- Validate correct firmware operation of hardware features with purpose of supporting drive programs with new features, and firmware workarounds
- Help develop, debug, and maintain validation firmware in all blocks
- Create tools to enhance test automation and efficiency
- Run the validations on all validation platforms: Cadence Palladium, FPGA and Disk Emulator
- Pre-silicon and Post-Silicon Validations
- Validation Plan and Results documentation
- Temperature and Voltage corner testing
- Interface to Applications Engineering and Drive Firmware teams.
- Provide reference firmware and application notes where needed.
- Serial port test scripts to operate testing.

Qualifications

- B.S. + 5-8 yrs related experience, or MS + 1-3 yrs related experience
- Embedded C/C++ is required
- Matlab
- PERL scripting
- ARM processor cores and tools
- Must be comfortable with both Unix and PC environments
- Oscilloscopes and arbitrary waveform generators (AWG's)
- Understanding of Verilog RTL a bonus

Job Title: ASIC/ Layout Design Engineer

Job Description:

JOB DESCRIPTION: -

The successful candidate will be capable of doing the following:

- Performing DFT/DFD RTL design using architectural specifications and AMD design flows
- Performing RTL implementation, integration/insertion, synthesis, equivalency checking, timing analysis and closure including defining constraints
- Writing and maintaining DFT/DFD documentation and specifications
- Performing scan ATPG/DRC, pattern generation and simulation
- Delivering production quality ATPG patterns to operations engineering team
- Providing DFT/DFD feature bring-up and pattern debug support to operations engineering team during first silicon bring-up, qualification and failure analysis
- Creating software, scripts and other support technology to enable successful creation of the items above
- Working with a multi-discipline and international team of engineers
- Understanding various technologies that must work with DFT/DFD technology such as CPU's, Graphics engines, memory and I/O controllers, etc.

PREFERRED EXPERIENCE:

- Candidate must have minimum B.Sc in Electrical or Computer Engineering (or equivalent)
- Good understanding of computing/graphics design architecture
- Familiar with ASIC design, fabrication, assembly and ATE test
- Knowledge of DFT techniques such as JTAG/IEEE standards, Scan and ATPG, memory BIST/repair or Logic BIST is a plus
- Good working knowledge of UNIX/Linux and scripting languages (e.g., TCL, c-shell, Perl)
- Familiar with verilog design language, Verilog simulator and waveform debugging tools
- Knowledge of EDA tools/methodology, such as synthesis, equivalency checking, static timing analysis is a plus
- Strong problem solving skills
- Team player with strong communication skills

Job Title: **ASIC Verification Engineer**

- Qualifications:
- BS/MS in engineering and 1-5 years of relevant work experience is required.
 - Extensive hands-on experience of SAS, SATA protocols.
 - Hands-on experience of PCIe interface is a plus.
 - Extensive hands-on experience with model development and design verification techniques.
 - Strong working knowledge in various hardware development tools like Formality, or Conformal compulsory.
 - The engineer must have a well developed ability to analyze specifications at the architecture and micro-architecture level to identify design improvements and to develop SAS protocol compliance tests.
 - Good knowledge of scripting in tcl or Perl.
 - Strong working knowledge in modeling and model creation/verification.
 - Fluency with Verilog, System Verilog. Knowledge in 'C', 'C++' is a plus.
 - Experience in RTL debugging using conventional EDA tools like NCsim or Synopsys VCS.

- Description: The engineer will be responsible for design verification of state-of-the-art SSD Controller. Typical activities include:
- Test Bench Creation
 - Behavioral Model creation
 - Writing verification plans and Execution
 - Test code creation
 - Analysis/Testing of RTL and suggest improvements.
 - Formal verification

Job Title: ASIC Verification Engineer

Job Description: Summary:

Verifying the ASICs at the heart of Cisco's networking products requires a unique combination of talents and a drive for perfection in the face of ever growing complexity. To accomplish this task, the design verification team requires software engineering acumen, cutting edge languages and EDA tools, deep understanding of the ASIC design, and broad understanding of the system context.

The successful candidate will join an experienced, dynamic group of engineers with many different areas of expertise. This is a great opportunity to work with the best in the industry, expand one's skills and experience, and be part of a team that is at the forefront of network technology innovation.

As a member of the design verification team, the successful candidate's responsibilities include:

- Reference model design and implementation.
- Test bench design and implementation.
- Test plan definition.
- Constrained random test development.
- Coverage specification & analysis.
- Performance testing.

The candidate is also expected to contribute throughout all stages of the ASIC development process, such as:

- Collaboration with architects, logic design, and software engineers.
- Micro-architecture and code reviews.
- Post-silicon validation and support.

Requirements:

- Working experience with C++.
- Strong object-oriented software design and programming skill.
- Solid verification skills: planning, problem solving, debug, adversarial testing, random testing.
- Outstanding oral and written communication skills.
- Degree in EE, CS or related discipline (MS preferred).
- At least 2 years (or 1 major project) experience using a modern hardware verification language (HVL), such as SystemVerilog.

Desired Experience:

- SystemVerilog, especially with the UVM framework.
- Shell scripts and Makefiles.

ASIC VERIFICATION ENGINEER

As a ASIC Verification Engineer at NVIDIA, you will verify the design and implementation of the industry's leading GPUs. In this position, you will be responsible for verification (including silicon validation) of the ASIC design, architecture, golden models and micro-architecture. You are expected to understand the design and implementation develop the test plan and verify the correctness of the design. You will be working with architects, designers, pre and post silicon verification teams to accomplish your tasks.

MINIMUM REQUIREMENTS:

- BS / MS with at least 1+ years of experience
- Exposure to design and verification tools (VCS or equivalent simulation tools, debug tools like Debussy, GDB)
- Expertise in Verilog/System Verilog or similar HDL/HVL
- Good debugging and problem solving skills
- Perl and C/C++ programming language experience desirable
- Good communication skills and ability & desire to work as a team player are a must
- Exposure to debugging in the lab with logic analyzers and oscilloscopes.

Audio Software Engineer

Job Summary

The Audio Design and Engineering team is seeking a highly motivated, hands-on Audio Software Engineer. In this role, you will support team efforts in the development of software and algorithms to improve audio performance and add features to our next generation of Apple products. You will work closely with hardware, application and QA team members to integrate solutions into products.

Key Qualifications

- The ideal candidate will have a proven, successful track record for contributing to high-visibility teams, preferably in high-volume consumer electronics environment.
Qualifications include:
- 2+ years' experience in audio software
- 2+ years audio DSP development
- Excellent C/C++ and Matlab coding skills
- Experience in the following strongly preferred:
- Audio software system design (drivers & frameworks)
- Bringing up audio hardware and prototyping systems
- Acoustic design
- Working with programmable DSPs

Description

You will be a key contributor to audio system design, expected to achieve performance goals and expectations. Responsibilities include but are not limited to the following: •Develop audio software focused on hardware features and support •Prototype audio DSP algorithms on new hardware platforms •Implement software and algorithms in C, C++ and assembly •Contribute to design of audio systems working closely with acoustics, EE and system teams

Education

MS in EE or CS

Automation Engineer

- Develop scripts and programs to automate applications and/or customer deployments
 - Identify and document areas of opportunity for improvements with recommendations.
 - System Management of all web applications and supporting tools
 - Collaboration with the other system and database administrators in the Cloud Operations
- Researching, evaluating, installing, creating and configuring new tools and processes
- Create/Modify technical process documentation
 - Assist in the training of the entry level Operations teams

Job Qualifications

- BS/MS in Engineering or equivalent experience
- Experience with administering Red Hat, CentOS, or SuSE servers
- Experience with cloud technologies like VMWare, Amazon EC2, Rackspace is a plus
- Strong shell scripting skills with BASH, Perl, or Ruby scripting languages
- Experience with Oracle 11g and MySQL.
- Experience with configuration management tools like Puppet
- Experience with Tomcat and JBOSS
- Experience with version control systems like Subversion, Git, or Mercurial.
- Experience with F5 load balancers
- Experience with Tomcat/JBoss, Apache, Linux
- Relevant Industry Certifications a plus (RHCE, VCP, SCSA, CCNA, JNCIA-FWV)
- Works effectively with a minimum of direction

BIOS Engineer

Area of Interest: Engineering

Essential Duties and Responsibilities:

- UEFI BIOS development for desktop and server
- Perform desktop, server board and workstation UEFI BIOS porting
- Device module codes for development
- Provide engineering support to customer BIOS issues
- Verification of BIOS System specification
- Control of project schedule from design phase to production
- Test of function and reliability and ensure product quality
- Work on related Linux kernel development & device drivers
- Study computer motherboard and BIOS system specifications and configuration requirements to plan and develop reliability and compatibility
- Study and analyze the design specification and system housing to optimize the system efficiency
- Consult and coordinate with other engineering staff to evaluate the design specification and functions

Qualifications:

- MS/BS in EE/CS with 2+ (MS)/5+ (BS) years in low-level software development experience
- Excellent understanding of PC/server hardware architectures, BIOS, and drivers

Physical Requirements:

The physical demands described here are representative of those that must be met by an employee to successfully perform the essential functions of this job:

- Able to sit for long periods of time
- Able to input data into a computer and work with a computer screen for long periods
- Occasional bending and standing
- Standing, walking, sitting, talking/listening, crouching or crawling, reaching with hands and arms
- Lift, carry, push and pull in excess of 25 pounds

Work Environment: The work environment characteristics described here are representative of those an employee encounters while performing the essential functions of this job:

- Office environment shared with other staff members
- Wet humid conditions, extreme heat or cold (weather), outdoor weather conditions

Build Engineer

- Define and execute best practices in software management, builds, and guaranteed product reproducibility
- Improve our build systems and integrated test automation framework, providing support for our continuous, nightly, and milestone builds across multiple streams of development
- Extend and improve configuration management setup that ensures the build farm is always in a known state. This includes maintenance of VM images, use of configuration management tools (like Puppet, Chef, CFEngine) as well as monitoring the health of the machines and the processes running on them.
- Contribute to the development and deployment of in-house and commercial products to help us manage, track and optimize our Agile development progress.
- Work closely with Program Management and Engineering teams to develop and deploy Agile team planning/tracking tools, software analysis tools, metrics collection & reporting systems, etc.

Qualifications

- Excellent communication skills, as this position involves a high degree of interaction with development team members and management
- Excellent debugging & problem solving skills
- Solid understanding of source control branching strategies that can support parallel development, along with source control merging best practices
- Ability of critically review processes and identify bottlenecks, potential points of failure, error checking, logging and make suggestions on how it can be improved
- Experience in continuous integrations and its best practices
- Comfortable in automating processes on both Windows and Linux platforms
- Ability to understand Java and C++ code plus familiarity with associated development environments/tools
- Comfortable in managing and deploying complex multi-platform (Windows, Linux, Mac) build systems
- Prior experience with:
 - o Administration of source control tools (Perforce, SVN, Git)
 - o Setup, integration and maintenance of development process tools (Reviewboard, Clover, Bullseye)
 - o Continuous Integration and automated build tools (Bamboo, Hudson/Jenkins, ant)
 - o Build/Testing environment setup (VMWare, Nagios)
 - o Scripting/Programming languages (Bash/Batch, Python, Java, PHP)

CAD ENGINEER

CAD Engineer on our VLSI team providing support for custom designs using Cadence and Synopsys tools.

RESPONSIBILITIES:

- General user support, tool customizations, techfile installation and support of design tools including Hspice, Hsim, DRC, LVS, StarRC.
- Foundry techfile installation
- Design flow debug and resolution
- Test development and automation
- P-cell development in advanced process nodes
- Tool configuration and application
- Configuration management
- General user support

MINIMUM REQUIREMENTS:

- BS or MS in electrical engineering or computer science.
- Experience in IC CAD support using both Cadence and Synopsys tools.
- Must have a solid understanding of IC design and devices, as well as analog / mixed-signal design flows.
- Must have experience with physical verification and characterization tools, including installation, testing and debugging.
- Must have experience supporting Cadence Composer Virtuoso, Layout tools, PCELLs development, and supporting and developing PDKs.
- Experience with advanced 28nm processes is preferred.
- Proficiency with programming in SKILL, Verilog, and PERL are helpful.
- Must possess excellent communication skills both written and oral.

DESIRED SKILLS

- VLSI design and/or device understanding
- Debugging software systems
- Speaking clearly
- Writing efficiently

DESIRED TRAITS:

- Collaborative
- Curious
- Flexible
- Self-motivated

Cloud Modeling and Data Analytics Engineer

Seagate delivers advanced digital storage solutions to meet the needs of today's consumers and tomorrow's applications. Through technology, leadership and innovation, Seagate continues to help individuals and businesses maximize the potential of their digital content in an ever-evolving, on-demand world.

Seagate is seeking an Engineer to join our Cloud Modeling and Data Analytics team in Longmont, Colorado.

Successful candidate will be responsible for developing solutions to design, engineering and manufacturing problems through modeling and data analysis and spearheading implementations. Creates formal networks with key decision makers and, when needed, serves as external spokesperson for the organization. The candidate will be offered wide latitude in determining objectives and approaches to critical assignments.

Beyond analytical thinking and problem-solving abilities, creativity is also a key aspect of this job, as unconventional concepts and out-of-the-box solutions are needed. The candidate must have the ability to work independently to shape and develop projects. Strong programming skills are necessary (C, R, Java, Python) and experience in numerical process modeling and optimization would be very desirable attributes. Data mining and data analysis background are also key aspects, along with solid knowledge of math and statistics. Knowledge of distributed and/or cloud computing and computer networks is required.

Qualifications

Qualifications

Distributed/Cloud Computing/Computer Networks knowledge
Cloud and network performance and power modeling
Modeling/Simulation Experience
Programming Experience with C, R, Java, Python

Preferred Qualifications:

SimGrid, CloudSim, GridSim Familiarity
Creative Problem-Solving
Interpersonal and Communication
Data Mining and Analysis
Experience Giving Presentations

Cloud Test Developer

- Test, and analyze cloud analytics schemas, procedures, and queries.
- Performance Tuning
- Trouble-shoot production systems as required.
- Work as an integral member of a high performance team. Estimate schedules for projects undertaken and complete them on time, while adhering to customer requirements, specifications, and quality standards.
- Assist other team members in resolving technical issues. Work closely with Software Development engineers in unit level testing, stress testing and problem resolution.

What You Need

Major on Computer Technology emphasis or equivalent experience.

Basic knowledge in the following:

- Database
- SQL
- Amazon Web Service
- Hadoop and Hive is a plus
- ETL, Reporting technologies is a plus.

Preferred Qualifications:

- Excellent verbal and written skills are a must.
- A good balance of independence and teamwork skills
- Ability to work with a global team and coordinate activities with US team
- Strong ability to document and formalize processes
- Good organizational skills and the ability to track and prioritize several tasks

Control Engineer

Job Description

The Controls Engineer is a key contributor to the overall success of the production operation. This position is a mix of industrial controls and mechanical design. This position reports directly to the Value Stream Manager and will have direct interaction with Product Engineering and Production as well as various support functions. Provide Production Support as needed. Troubleshoot, determine root cause, and repair production equipment as it pertains to motion control as necessary. Assist maintenance as required. Development of PLC control programming and HMI applications, primarily focused around the Rockwell RS Logix family of PLC and the FactoryTalk View Studio HMI software platforms for new equipment.

Generate electric circuit drawings, various wiring diagrams, power-system schematics, and single line drawings to document designs.

Design electrical and electronic hardware systems for machine control.

Defining, requesting and coordinating the purchase of electrical materials, components, equipment, and support services for the construction, assembly, and conditioning of prototype parts and machines.

Provide training support to Operations personnel in the proper setup, function and control of automated manufacturing equipment.

Performing start-up services and technical support for new equipment.

Job Requirements

PLC programming/troubleshooting, experience with AB.

HMI configuration experience including graphical screen development using AB.

Experience with industry standard communication protocols such as Devicenet, Ethernet /IP.

Extensive knowledge and hands on experience in developing complex control architectures.

General knowledge of applicable electrical standards such as, IEC, UL, and the National Electric Code

Excellent verbal and written communication skills. Strong interpersonal skills.

Knowledge and experience with high voltage systems a plus.

Knowledge of SAP and Autodesk Inventor a plus.

BS degree Electronic Engineering, Electrical Engineering or equivalent.

Control Engineer

Primary responsibilities of the Electrical (Controls) Engineer will include, but are not limited to: designing, planning, and supervising the installation and maintenance of electrical/electronic controls and instrumentation for machinery and support equipment used in the manufacturing process.

- a) Utilize PCs and various tools to diagnose, correct and improve machine and process performance.
- b) Implement cost savings projects by evaluating and implementing new and improved processing techniques or electrical/controls enhancements.
- c) Design, plan, and implement electrical/controls upgrades on existing equipment for improved performance.
- d) Assist in the training and development of electronic technicians and other electrical engineers. Support manufacturing by assisting electronic technicians in troubleshooting and repairing complex electrical and electronic equipment.
- e) Provide innovative technical leadership to other engineering disciplines in the use of electrical and electronic products in their projects.
- f) Evaluate and improve manufacturing process work instructions and electrical/electronic drawings. Maintain electrical and electronic drawings, ladder logic diagrams, computer program listings, and other documentation using AutoCAD computer program.
- g) Recommend and review preventive maintenance programs, gauging systems, and machine controllers. Complete equipment proposals and purchases for these types of programs and systems.
- h) Evaluate new technologies in the electrical and electronic field for the purpose of incorporating these technologies in this manufacturing process. Utilize these technologies to improve production processes, part quality, and safety.
- i) Implement new types of testing and calibrating instruments to aide electronic technicians and other engineers troubleshoot and repair equipment.
- j) Evaluate machine vendor proposals for effective processor and programming requirements. Lend project technical and management support as necessary on new capital programs.
- k) Support ongoing improvements to OEE.

Basic Qualifications (Including Educational Requirements) Basic Qualifications:

- 1) Bachelor Degree in Electrical Engineering, Mechanical Engineering, Aerospace Engineering or Engineering Technology from an accredited institution.

Design Engineer

Position Description:

Candidate will contribute as Sr. Engineer within a technical product development environment while assigned to multiple projects. The candidate will be responsible for the detailed design of products, along with prototype construction, test fixture construction, and the execution of some prototype testing. Position will require cross- functional involvement with R&D, Electrical Engineering, Quality, Supply Chain, and Technical Services personnel.

Typical Roles/Area of Focus:

Incumbent's responsibilities will generally focus on the following:

- General Engineering
- General Design/Tool Design
- Drafting/Design using Solidworks
- Prototype fabrication using machine shops tools
- Prototype testing
- Project Management

Level of Responsibility:

- Responsible for several components of a project or a complex component of a larger project
- Contributes to their team or department by applying their knowledge of one or more functional areas
- Fully developed professional able to work without direct guidance or direction from others
- Has in depth knowledge of the discipline and role

Typical Educational Requirements:

- BA/BS degree w/4 -7+ years experience
- MA/MS degree w/2+ years experience
- PhD degree w/0 - 2+ years experience

The candidate must be able to manage small projects or portions of large projects independently. Experiences in the areas of mechanical design, drafting, and prototype construction are key requirements of this position. Experience with the design of injection-molded and extruded plastic components and/or stamped metal parts using Solidworks as a design tool is essential. Ability to use standard machine shop tools such as a milling machine, lathe, etc. in order to fabricate prototype parts and test fixtures is required.

Job Title: Design Verification Engineer

Job Description:

Job Title: MTS – Design Verification Engineer

Educational Requirements:

- MS degree in Electrical or Computer Systems Engineering or Computer Science + 1.5 years practical experience in design verification

or:

- BS degree in Electrical or Computer Systems Engineering or Computer Science + 5 years practical experience in design verification.

Experience required:

- Development and execution of pre-silicon verification test plans
- Development of verification environment and infrastructure
- Verification of complex microprocessor SOC's (preferably with ARM or multiple architectures)
- Development of directed and random verification tests to validate IP/chip function
- Proficiency in debug of Verilog RTL and gate-level simulation, at the IP and/or chip-level
- Mastery of industry standard simulation tools (VCS, Verdi)
- Mastery of at least one verification language (preferably SV/UVM, though the following also acceptable: SV/VMM, SV, C++, SystemC, Vera, E)
- Mastery of a scripting language (Shell, Perl, Python, Ruby)
- Verification functional coverage using industry standard coverage analysis tools/methods.
- Ability to replicate functional issues found post-silicon; review/enhance tests to verify bug fixes

Digital Design Engineer

This is a unique opportunity to join a team who produce world class products and contribute to their development initially under close supervision. Engagement with key customers, applications engineering and marketing is also required to derive target specifications. The role would involve the person in not just the design of the products but in the definition and feasibility work beforehand. During the design phase, consideration is given to high level design (architecture definition) followed by circuit development using state of the art CAD tools. After the design and layout phase, close interaction with other groups such as evaluation and test is important to ensure that the product can be manufactured in a reliable manner with guaranteed performance for life. To facilitate this, design staff are encouraged to develop project management and team leadership skills.

The development of new and innovative converter architectures using the latest process technologies is also vital to long term success. This can involve interaction with design groups in other locations resulting in multi-site projects. The product line has design engineering teams based in Wilmington, Newbury, Limerick and Cork. The position advertised is for Newbury.

Core Responsibilities include:

Participation in the product definition process and feasibility studies

- Circuit simulation and iteration to meet the performance goals
- Supporting behavioral simulation of the full converter
- Supervision and analysis of circuit layout and lab experiments
- Collaboration with other groups (evaluation, test, applications, reliability) to ensure the release of robust silicon.

Qualifications

:

The ideal candidate should have good practical knowledge of Analog and Digital Electronics, be capable of accepting responsibility, good interpersonal skills and a willingness to learn. The person should show initiative/innovation in the design and development of our converters:

- Degree in Electrical / Electronic Engineering or equivalent.
- Ideally experience of schematic entry and layout tools (e.g Composer/Virtuoso from Cadence)
- Ideally some working knowledge of circuit simulation using spice.
- Ideally some knowledge of RTL (Verilog), logic synthesis, timing analysis, design for testability (DFT) and automatic test pattern generation (ATPG) techniques.
- Be self-motivated with a willingness to learn, good interpersonal skills and technical problem solving skills.

DISPLAY SOFTWARE ENGINEER

We are looking for a system software engineer to develop and support graphics on Windows operating systems.

RESPONSIBILITIES:

- Develop and maintain features to support current and upcoming Windows operating systems.
- Develop features specific to new hardware graphics architectures and support these new architectures through all design phases.
- Work with other internal teams (software, hardware, architecture, and OEM support) to understand and resolve issues.

MINIMUM REQUIREMENTS:

- Minimum of 3+ years of industry experience for candidates with BS degrees, will consider candidates with less experience that have graduate degrees.
- Degree in Computer Science, Mathematics, Computer Engineering, or Electrical Engineering.
- Strong programming skills in C/ C++
- Strong written and oral communication skills for technical discussions with an international engineering team.
- Strong problem solving and debugging skills.
- Practical understanding of operating system internals (Windows, Android, or other operating systems).
- Experience with version control tools.
- An ideal candidate is comfortable working in a team or independently depending on the project scope, and is committed to delivering high-quality software on schedule.

Electrical Design Engineer

Summary: This position is accountable for implementation of Electrical Design concepts used in customized designs, New Product Development, value engineering and product support projects through design, prototyping, testing, tooling and pilot production under the guidance of an Engineering Mentor for Arresters. This role also provides technical assistance to solve manufacturing and field problems, assists in answering product questions and providing detailed manufacturing support.

This person:

- Provides design work on products to analyze performance and improve designs.
- Provide technical support and maintain liaison with Manufacturing regarding technical manufacturing problems relating to design, specifications and functional performance. When appropriate, authorize deviations.
- Responsible for the preparation of required engineering reports and records pertinent to assigned projects and product areas.
- Work with purchasing (Sourcing?), local US as well as International Sourcing, in establishing solid vendor (supplier?) relationships for purchased parts.
- Identify opportunities within products and/or applications to increase quality, decrease costs and increase value. Review and analyze competitor's design
- Plan and implement research methodology and procedures to apply principles of electrical theory to engineering projects.
- Review technical drawings and specifications of electrical systems to ensure that the product conforms to industry standards and customer requirements.
- Compile data and write reports relating to engineering studies and projects.

Demonstrated Performance:

- Technical Innovation -- resolution of technical manufacturing problems, product testing and certification programs.
- o Diagnose product issues and provide innovative solutions
- Critical Thinking -- Using logic and reasoning to identify the strengths and weaknesses of alternative solutions, conclusions or approaches to problems.
- Complex Problem Solving -- Identifying complex problems and reviewing related information to develop and evaluate options and implement solutions.

Leadership/Talent Development:

- Acts in an ethical and truthful manner, leading by example and earning the respect and trust of co-workers, customers, and other stakeholders
- Sets aggressive targets, accepts consequences of actions, and holds others accountable
- Meets the expectations and requirements of all by making customer needs a primary focus
- Delivers commitments, producing work of the highest quality in a timely manner
- Deals respectfully and fairly but decisively with difficult circumstances
- Understands priorities and balances short-term and long-term requirements
- Communicates openly and honestly in a clear and concise manner on an ongoing basis
- Modifies plans to meet changed circumstances by being flexible and adaptive
- Demonstrated skills in Electrical Engineering analysis and tool capabilities.

Embedded Firmware Engineer

An Embedded Firmware Engineer with approximately 2+ years experience is preferred.

Description:

We are seeking a fulltime firmware engineer to assist in the development of embedded products. The candidate must be motivated, ready for an opportunity to work in a fast paced, team-oriented environment. The qualified individual will be responsible for learning new protocols, porting, and writing firmware that is used in our products. The Firmware Engineer will work closely with the software and hardware teams to develop and integrate embedded code in a variety of processors: PICs, ARMs, etc. The type of job function will include code development, code debugging, and performance improvement, etc.

Requirements:

- MS EE/CS/CE;
- At least two years in firmware experience;
- Experience in programming real-time/embedded applications;
- Experience with C/C++, Java, and Assembler is required;
- Experience in Linux OS using source code control systems;
- Experience in ARMs processors;
- Experience in IPMI protocol, virtual media and web server is a plus;
- TCP/IP network protocols and typical network operation practices is desired;
- Self-motivated engineer who can follow a project through with minimal day-to-day supervision.

Embedded Software Engineer

Job Summary

Apple's Platform Architecture Engineering group is seeking an excellent Software/Firmware Engineer with working knowledge of general digital/EE design. The engineer will be responsible for development of MacOS, iOS and micro-controller code, investigating new technologies, prototyping concepts, and developing instrumentation/tools. This position is the focal point for HW/SW integration, requiring a self-driven engineer with great initiative.

Key Qualifications

- Excellent programming skills and knowledge of C and OOP practices.
- Development experience on Unix, Linux or Mac OS X
- Experience with micro-controllers and working with new peripherals, features, and development environments.
- Ability to read serial bus traces, board schematics, use an oscilloscope and logic analyzer.
- Experience with board bring up and digital hardware.
- ARM, USB, I2C, SPI, Lua, Objective-C, OS X driver, GIT and SVN experience a plus.
- Experience with FPGA and hardware simulators a plus.

Description

- Embedded software/firmware design and implementation - Interface with mechanical, EE, SW and other teams on device design. - Thoroughly document and support implemented code. - Must be a team player with excellent communication, negotiation and presentation skills.

Education

* BS/MS CS/CE/EECS and experience in embedded software solutions.

Job Title: **Embedded Software Engineer**

Qualifications: Qualifications Masters or (Bachelors degree + 5 years experience) in the field of computer science or computer systems engineering, electrical engineering or equivalent.
Experience developing software for embedded systems.
Experience developing drivers and/or applications for the embedded operating systems (Linux/Android/QNX).
Proficiency in C or C++. Experience with ARM Assembly language helpful.
Ability to read and understand schematic diagrams and component datasheets.
Experience debugging hardware and software with the use of symbolic debuggers, oscilloscopes and logic analyzers.
Familiarity with general operation of source control tools.

Description: Seeking a candidate to develop boot software/firmware for Marvell's Applications and Communication Processors used in wide array of handheld and cellular products. The position is located in Marlborough Massachusetts.

The candidate will be exposed to a variety of tasks related to the design, development and testing of boot code in cutting edge mobile/embedded processor system designs. Their responsibilities will include but are not limited to the following:

- * Design boot code for Marvell's secure BootROM's.
- * Design Operating System independent first level boot loaders.
- * Incorporate new boot media technologies.
- * Ensure boot interoperability with future mobile Operating Systems.
- * Use Marvell's existing system provisioning tools to enable trusted boot solution for new products.

Failure Analysis Engineer

Seagate is looking for a Failure Analysis Engineer located in our Design Center in Shakopee, MN reporting to the Senior Failure Analysis Manager. The ideal candidate will perform “root cause” failure analysis on disc drives to include Engineering support and documentation. The candidate will develop, initiate, and document methods and procedures for root cause analysis as well as conduct bench level training for FA Lab Technical personnel as required. Candidate will work closely with Design, Systems and Manufacturing Engineering groups to identify and resolve product issues to include new disc drive features. In addition, the candidate will perform statistical analysis on Customer, Factory, and FA data to identify Customer or product issues. The candidate will be required to interact with Technical peers, and Management personnel.

Provide technical support to Technical peers. Conduct analysis of issues to determine “root cause” mechanisms. Typical duties: Communicates results of analysis to Technical peers and support Management. Create MatLab routines to be used in disc drive analysis. In addition, write MACROS or programming scripts as needed.

Work on issues that impact product design, product qualification, or technologies and features.

Qualifications

Required:

- The candidate must have a BS degree in EE, CE or related field with 0-2 years of experience.
- Candidate should have an excellent background in SCSI/SAS/FC and SATA interfaces.
- Knowledge of MatLab scripting and good general programming skills (C, C++, Python, Visual Basic).
- Must be capable of working with general direction and supervision to include providing work direction to support personnel when necessary.

Firmware Engineer

QLogic, a leading supplier of high performance storage networking solutions, simplifies the process of networking storage by providing the only end-to-end SAN infrastructure in the industry. QLogic builds solutions based on all storage network technologies including Fibre Channel, iSCSI, and FCoE/Ethernet.

We have an immediate need for a Software (embedded firmware) Engineer for our Mt. Rainier technology. In this position, your primary responsibility will include development of software and firmware including embedded kernel modules, drivers, diagnostics (BIST, offline, online, and manufacturing test), u-boot (configuration, modifications and enhancements), BIOS (configuration and enhancements), UEFI and manufacturing tests for SAN products. You will participate in the product development from introduction to release and support of released products. This would include analyzing requirements, design, development, test, release, documentation through the development life cycle, support, duplication of issues, and customer satisfaction.

You will work with software engineers in a small team environment. Assist and perform research, test and/or development within given product parameters and/or support new or existing SAN router products and associated equipment.

- Strong C and C++
- Assembly language
- Linux OS programming (kernel mode)
- JTag debugger experience
- Debug/problem analysis techniques
- Hardware debugging, schematics
- Tools for debugging, oscilloscope, DMM, Data recorder, etc

Required Skills

- Experience in embedded software/firmware development of products with PCIe interfaces, I2C, DRAM, network interfaces, and protocol controllers such as Fibre Channel and Ethernet.
- SCSI experience.
- Prior experience with storage protocols and applications, Fibre Channel, iSCSI and SSD technologies is a plus.
- Ethernet and Converged Networking experience is a plus.
- Experience with microprocessor architectures such as x86, ARM, MIPS, or PPC.

Firmware / Test Process - New College Grad

Seagate delivers advanced digital storage solutions to meet the needs of today's consumers and tomorrow's applications. Through technology, leadership and innovation, Seagate continues to help individuals and businesses maximize the potential of their digital content in an ever-evolving, on-demand world.

Are you an engineer seeking an exciting opportunity to get involved with Firmware Engineering? The Longmont, CO design center is now hiring. This is an exciting opportunity to get involved with new product development and manufacturing. This position reports to the Senior Engineering Manager.

The Firmware Engineer will be responsible for embedded real time code development. They will also implement, debug and deploy embedded interface (SAS,/SATA/USB) firmware in new data storage products

Qualifications

REQUIREMENTS

- Bachelors/MS degree in Electrical Engineering, Electrical and Computer Engineering, Computer Science, Physics or Math
- 0-2 years of experience working with real time embedded firmware, experience developing (design, implementation, debug) of real time "C" code.
- Analytical skills.

PREFERRED QUALIFICATIONS

- Masters degree in Electrical Engineering, Electrical and Computer Engineering, Computer Science, Physics or Math
- Experience using code development tools (emulators, trace, version control, compilers, linkers, analyzers).

Firmware Engineer

Do you have an embedded firmware or hardware background, or both? We are seeking a new team member to join our development team. This position reports to the Managing Principal Engineer. This is an exciting opportunity to join our Firmware Engineering organization. In this role, you will be responsible for the following:

- You will perform verification testing and debugging of ASIC designs against firmware and communicate design plans for evaluation.
- Provide analysis (discover, document, communicate) of hardware and firmware functional problems and design flaws.
- Regress hardware and firmware solutions to problems found and verify functionality.
- Debug and verify that operation metrics fulfill design requirements and specifications.
- Implement model designs for software, firmware, and hardware to demonstrate their functionality.
- Responsible for co-simulation and verification of disc controller, host interface, and buffer interface functionality using drive firmware.
- Create new test benches and maintain support in leveraged simulation environment.
- Contribute to the application of advanced verification methodologies.
- Conduct assessment and design of firmware, hardware modeling software utilities, and scripting tools to meet requirements and provide customized solutions.
- Develop and maintain transactors/models.
- Develop drive firmware in C/Assembly for co-simulation.
- Support feature integration and perform failure analysis for issues revolving around Co-simulation environment for targeted SOC (System on Chip).

Qualifications

Requirements:

- Masters in Electrical or Computer Engineering
- 2-4 years of Hardware Verification or Embedded Firmware experience
- Experience with Verilog programming.
- Experience with C programming
- Experience with Perl and/or Python scripting.
- Ability to support after hours calls

Preferred Qualifications:

- Experience with C++ programming
- Experience with Systems Verilog programming
- Experience working in a team environment
- Experience communicating with across an organization (both written and verbal).
- Ability to mentor and coach both technical information and business acumen.

Firmware Engineer

Area of Interest:

Engineering

Position Duties:

- Firmware or Software Engineer with strong SAS/SCSI experience, SAS, Serial Attached SCSI, SATA, SCSI, iSCSI, FC, Fibre Channel, RTOS, NAND, SSD, HDD, Micro-controller, ARM, MIPS Architecture, UART, I2C, SPI, GPIO, SAS/SATA Embedded design and verification.
- Firmware Engineer – Embedded Engineer - SAS - Serial Attached SCSI - Firmware - Software Engineer - Firmware Engineer - Embedded Software Engineer – Micro-Controller RTOS - SATA - SCSI - iSCSI - FC - Fibre Channel - HDD - Hard Disk Drive - SSD - Solid State Disk - NAND - PCIe - MIPS - ThreadX - embedded – SOC.

Position Requirements:

- years of software/firmware development for embedded systems
- 1 year of experience with storage protocols; SAS, SATA, SCSI, iSCSI, or Fibre Channel / FC
- Must have experience with RTOS, ThreadX RTOS is a plus
- Experience or strong knowledge with either of the following: NAND flash management of Solid State Disk / SSD
- Must have experience delivering storage technologies
- Must have strong C programming experience
- Tools development for support of firmware products
- Development of tools issue tracking and resolution
- Knowledge of scripting languages like bash, Tcl/Tk, etc...
- Experience writing drivers for embedded microprocessors is a plus; UART, I2C, SPI, GPIO
- Experience developing on MIPS Architecture is a plus!

Minimum Education Requirements:

- BS or MS in Computer Science or Electrical Engineering

Firmware Engineer

Job Description

QLogic, a leading supplier of high performance storage networking solutions, simplifies the process of networking storage by providing the only end-to-end SAN infrastructure in the industry. QLogic builds solutions based on all storage network technologies including Fibre Channel, iSCSI, and FCoE/Ethernet.

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You will work with software engineers in a small team environment. Assist and perform research, test and/or development within given product parameters and/or support new or existing SAN router products and associated equipment.

- Strong C and C++
- Assembly language
- Linux OS programming (kernel mode)
- JTag debugger experience
- Debug/problem analysis techniques
- Hardware debugging, schematics
- Tools for debugging, oscilloscope, DMM, Data recorder, etc

Required Skills

- Experience in embedded software/firmware development of products with PCIe interfaces, I2C, DRAM, network interfaces, and protocol controllers such as Fibre Channel and Ethernet.
- SCSI experience.
- Prior experience with storage protocols and applications, Fibre Channel, iSCSI and SSD technologies is a plus.
- Ethernet and Converged Networking experience is a plus.
- Experience with microprocessor architectures such as x86, ARM, MIPS, or PPC.

FIRMWARE ENGINEER

Firmware Engineer SSD - Firmware Developer Solid State - Enterprise Storage - Embedded Software FTL

Based in San Jose California, we are an established consumer products company and one of the leading memory device companies in the world. We are building a new Enterprise Storage Division to expand our product offerings and really make an impact in the Enterprise Storage and Solid State Drive space.

We have had a few key industry hires to start the team building and product development, and we are now looking for some senior, senior staff and principle firmware engineers with enterprise storage / SSD experience to join our growing team and new R&D Lab dedicated to Enterprise Storage and Technology Convergence. We are in a great position to offer the creativity and R&D Lab environment of a nimble start-up with the backing of a multibillion dollar organization.

Primary responsibilities: To develop enterprise computer storage products for large-scale, high-technology environments of the modern enterprises and to create products that have higher scalability, higher reliability and better fault tolerance.

Must have skills:

- 1.) Proficiency with C, C/C++ and ASM / Assembly
- 2.) Experience with mass storage devices and computer bus interfaces: SAS, SATA
- 3.) The ability to see tasks end to end. To be able to analyze, design and develop solutions both individually as well as part of a team.
- 4.) Excellent problem solving and technical design ability
- 5.) Passion and desire to create market leading products that adapt and improve upon existing technologies

Firmware Engineer

Job Summary

Imagine what you could do here. At Apple, great ideas have a way of becoming great products, services, and customer experiences very quickly. Bring passion and dedication to your job and there's no telling what you could accomplish. As part of a highly talented FPGA and emulation team you will join in the emulation and debugging of highly-complex SoCs?. You will interface with all related disciplines (logic design, emulation, design verification, silicon validation, OS software, device drivers).

Key Qualifications

- excellent C programmer
- familiar with boot code, low-level OS code, drivers, computer initialization, IO devices, SoC? bringup, debuggers
- solid understanding of SoC? architecture
- embedded software experience.
- Desired:
- experience with debugging implementations in FPGAs and emulators
- scripting (any of Perl, TCL, Python, Lua, bash)
- ARM processor, V7/Thumb2 debug experience.
- ability to debug in chip simulation/emulation environments with chip design tools (Verilog/VHDL, VCS, Verdi, etc.)

Description

As FPGA Firmware Engineer you will be responsible for firmware development and support of Apple's FPGA prototyping effort and will be expected to : Develop/test/release/maintain/enhance micro-controller code for control and communication of prototyping subsystems.

Develop/test/release/maintain/enhance low-level software to validate FPGA prototypes and to validate functionality of SOC sub-systems. Bringup and debug of SOCs in development on simulation/emulation/prototyping platforms. Contribute productively in cross functional debug efforts. Run/validate software developed on silicon if needed.

Job Posting Title **Firmware Engineer**

Business Unit Mobile and Wireless Group

Job Description Broadcom is developing solutions supporting the next generation of 3G and 4G high-speed mobile technologies and baseband processors for use in cellular handsets, PC cards and other wireless-enabled consumer electronics. As wireless devices including handsets, data-cards and smart-phones, emerge as the dominant platform for mobile media convergence they are driving the need for higher data rates. Broadcom will help to build a new generation of mobile devices supporting new applications such as video telephony and messaging, graphics-rich sports and news clips, interactive gaming, location-based services and improved web browsing.

MPS ASIC Validation Internship - Execute concurrency test methods for evaluation of cellular baseband SoCs. - Evaluate test results against specifications for mixed signal baseband devices. - Work closely with the DVT engineers and chip designers on the concurrency test requirements. - Devise bench stress tests for evaluation of cellular baseband SoCs. - Implement automated bench level device screening procedures by using programmable test equipment. - Measure results of new cellular baseband SoC devices. - Generate comprehensive test reports

Job Requirements Programming familiarity/background in embedded processor (ARM preferred) C/assembly language programming at the register level is required - Programming familiarity/background in any of Visual Basic, Visual C++, or similar applications programming language is desirable - PC and operating system and applications knowledge is required - Excellent communication and technical writing skills - Understanding of electronic circuits and systems - Familiarity with reading PCB schematics - Operation of high speed lab equipment, digital scopes, Spectrum Analyzers, logic analyzers, waveform generators and function generators is desired - A current Computer Science / Electrical Engineering student, preferably working toward a degree in Computer Science or Electrical Engineering - Must be in a Master Degree program or higher.

FIRMWARE TEST ENGINEER

BS, 5-7 Years, MS with 2-4 years

- The ideal candidate will have demonstrable technical understanding in one or more of the following: SAS, SATA, and/or PCIe Gen-2 and Gen-3.
 - Must have proven experience using modern Software Test Engineering concepts.
 - Must have excellent server and storage debug experience.
 - Experience in storage architectures, principles, storage topologies, and performance concepts.
 - Ability to perform Server System Administration (build, customize, provision, and troubleshoot) on the latest versions of: Windows Server, Linux, and VMware (or equivalent Hypervisor). For example: RHEL, SUSE, Windows 2008 Server, ESXi.
 - Experience with test tools such as: ClearQuest, Jira, or Bugzilla and either commercial or homegrown test automation tools.
 - Moderate C/C++ programming experience as well as Perl, Python, or equivalent scripting language for developing and using test tools is required.
- The Customer Focused Test team is responsible for server and storage integration testing of LSI Accelerated Solutions Division (ASD) products. This type of testing is critical to the success of our products in a constantly changing environment. This position requires constant personal skills development to keep up-to-date with the latest LSI products, the newest enterprise-class servers, and the latest operating system environments. Our customers require us to execute high-quality, detailed testing of our products so they may use and/or resell the products worldwide. At LSI, we often work closely with the customer defining test standards and/or refining statements of work. Attention to details is the top prerequisite.

Job Description: Responsibilities:

- Create and execute test plans (test suites, test sets, test cases and test runs) for PCIe flash storage devices (including Flash Storage Processors).
- Ensure test plans are clearly defined to validate software, firmware and hardware features in both standard and customer-specific environments.
- Work closely with Software Development in SCRUM style Sprint planning and execution.
- Work closely with Program Management teams to help plan and execute per customer and marketing requirements.
- Troubleshoot and diagnose software issues and provide CLCA upon resolution.
- Track bugs, test bug fixes, and report test progress in a timely fashion.
- Research and implement new and innovative ways of testing hardware and firmware while driving internal process improvements.
- Communicate (US and Worldwide) with internal groups and customers on test status, schedules, and issues via e-mail, conference calls.

FPGA Engineer

Responsibilities:

- Design, development, and testing of complex high-speed FPGA designs in VHDL
- Timing closure of FPGA designs
- Testing of FPGA designs in hardware
- Documentation of functional and design specifications

Requirements:

- Knowledge of VHDL or Verilog
- Preferred candidates should demonstrate a minimum overall GPA of 3.0
- Internship candidates should be college students seeking a Bachelor's or Master's degree in Computer Science, Computer Engineering, Electrical Engineering or other related technology majors

Desired:

- EE119
- Knowledge of TCL, Python, or C

FPGA Engineer

Engineer needed to for porting (build and integration) RTL to FPGAs that connect to client platforms. These platforms will be used for testing and validation of the IP.

Tasks to include:

- FPGA builds of IP running through FPGA Xilinx tools flows
- Writing new scripts and/or modifying existing scripts to make the build flow efficient
- Hardware debug of the FPGA and platform

Requirements:

- Well versed with Verilog, FPGA tools, Scripting languages such as PERL, Python, C/C++, Windows and Linux OS and the OS software stack
- Good communication skills and problem solving skills to resolve issues in an expedient manner as possible

Hardware Design Engineer

Position Duties:

- Responsible for design schematics, layout check, BOM creation, and specifications or modifying and enhancing existing designs for server mother board products.
- Collaborate with validation engineers and labs to discover and solve any quality-related issue of designs in early stage to ensure and enhance the quality of the products.
- Create fluent technical documentation including HW specifications, layout instructions, test procedures, etc.; Resolve complicated issues and work on critical engineering problems and/or projects;
- Ensure product conform to engineering specification, regulatory and customer performance expectations;
- Transfer products to manufacturing with clear documentation, and help C.M. and technical support to solve product problems;
- Perform other duties as assigned.

Position Requirements:

- BSEE or MSEE; 2+ years of experience working with x86 server / workstation boards is a plus;
- Familiar with x86 server MB design, architecture and applications;
- Wide knowledge of PCB layout, OrCAD/Allegro tools, circuit design, and PCB structure.
- Knowledge of high speed circuit design and voltage regular circuit design;
- Digital scope and high speed bus measurement;
- Excellent organizational skills and problem solving capability;
- Self-motivated, team player with ability to work in fast-paced environment.

Hardware Debug Engineer

Essential Duties and Responsibilities:

- Design verification for quality assurance
- Board level/system level debugging
- Customer support for technical issues and product specifications
- Product development, specification and review
- Help testing and testing techniques/procedures and production quality
- Level 3 technical support debugging root cause for customer issue
- Work with R&D to have solution for root cause and follow up for Eco
- Provide root cause analysis for customer FAR
- Support test lab in system validation and improve test methodology
- Support product manager and sales for technical issues
- Monitor RMA and work with R&D and production to decrease RMA

Qualifications:

- BSEE plus 5+ years computer system experience, MSEE preferred
- Relevant experience in PCB fabrication/assembly, board level bring-up, compatibility and reliability testing, and in-depth understanding of the PC industry
- Strong circuit analytical ability in circuit design
- Knowledge and understanding of computer systems
- Proficiency in English, good communication skills (written & verbal)
- Ability to work in a team environment and cross department development

Physical Requirements:

The physical demands described here are representative of those that must be met by an employee to successfully perform the essential functions of this job:

- Standing, walking, sitting, talking/listening, crouching or crawling, reaching with hands and arms
- Lift, carry, push and pull in excess of 25 pounds

Work environment:

The work environment characteristics described here are representative of those an employee encounters while performing the essential functions of this job:

- Primarily works indoors in an office setting with controlled climate conditions
- May be susceptible to outdoor weather conditions (i.e., cold, heat, rain, and/or wind)

Hardware Developer

Hardware Developer positions available in Santa Clara, California. Evaluate reliability of materials, properties and techniques used in production; plan, design and develop electronic parts, components, integrated circuitry, mechanical systems, equipment and packaging, optical systems and/or DSP systems. Responsible for designing, developing, modifying and evaluating electronic, electro-mechanical or mechanical components, assemblies or integrated circuitry for hardware systems for the external market. Design, analyze, test, and process develop using engineering principles and methods. Technical disciplines may include electrical and logical design of printed circuit boards or integrated circuits; mechanical design of electronics enclosures or integrated circuit packaging; and embedded software/firmware design.

Job Requirements

Employer will accept Master's degree in Computer Science, Elec. Engineering or related technical field and 1 year of work experience in job offered or 1 year of work experience in a hardware developer-related occupation. Education or experience to include: VLSI Design; Physical design automation and physical design placement and routing; EDA tools development; EDA algorithm and data structure; Semiconductor process technology; Industrial EDA tool usage; C++ programming; PERL programming; Designing and developing router using C++ programming language. Experience may be gained concurrently.

Hardware Engineer

Job Category: Intern - Engineering

Job Description:

Description

As a member of an ASIC design team, you will be responsible for implementing test plans to verify block, cluster or chip level functionality in a complex System-On-Chip environment. This will include running simulations on powerful servers to simulate the behavior of the block/cluster/chip under various stimuli, debugging issues as they arise and assisting senior engineers in solving such issues.

Benefits to You

- Work with “bleeding edge” technology and help influence the next generation of the Internet
- Work alongside and learn from leading ASIC engineers that have designed chips for the CRS-3 router
- Get exposure to the latest and greatest software tools used in the ASIC design process
- Get an inside look at a career at Cisco

Skills Desired:

- Interest in Digital Design
- Experience with Verilog/VHDL and an object oriented language
- Exposure to Digital Design and Verification would be an asset
- Linux/Unix

Software Tools and Languages Used at Cisco:

- The design and verification are both done in SystemVerilog
- Synopsys VCS and DVE are used for running simulations and debugging
- Clearcase is the version management tool

Education

Computer Engineering/Electrical Engineering; Open to both undergrad and grad students.

Hardware Engineer

Perform microwave circuit and packaging design, modeling, and layout for multifunction microwave modules and microwave integrated circuits. Involves combination of MMIC and discrete active/passive device application to solve various microwave hardware requirements.

Responsibilities include module and circuit design using standard design tools (ADS, AWR, Sonnet, HFSS, etc.). Will work with other engineers to parse out and negotiate requirements, specify and develop custom or semi-custom devices, determining appropriate technologies to apply. Additional responsibilities include working with inside and outside suppliers, as well as internal manufacturing to assure designs are producible. Also involves testing of developed hardware.

Required Education:

Bachelors/MS and 0+ year(s) related experience

Required Experience:

Bachelor's degree in an Engineering or Scientific field or equivalent experience, Employees in California must have a Bachelor's degree in the applicable field, or an applicable certification, equivalent certificate or any required license.

Equivalent experience will not be considered.

Required Skills:

BSEE/MSEE with some course related experience and or concentration in microwave/RF design.

Electronic Solutions provides systems and products that maximize the effectiveness and safety of fighting forces and first responders. Its offerings include infrared countermeasures and electronic protection systems for military and civilian aircraft; information management systems; signal acquisition, analysis, and exploitation systems that optimize use of the electromagnetic spectrum by friendly forces while denying its use by adversaries; interoperable communications systems for firefighters and other first responders; thermal weapon sights; precision targeting systems; situational awareness systems for ground vehicles; night-vision systems; identification friend or foe systems; and radiation-hardened electronics. Electronic Solutions also includes the group's Technology Solutions unit, which joins the business with government research organizations and industry to develop and demonstrate innovative system concepts in such areas as situational awareness, communications and networking, and precision targeting.

Hardware Systems Engineer

Job Summary

Apple is seeking an EE to work on electrical & system design integration of the Mac computer system. The candidate will be responsible for the design, implementation, integration and qualification of a complex computer system.

Key Qualifications

- Skilled in computer system architecture, schematic design, digital and analog circuits, power and voltage regulator integration, high speed design and signal integrity concepts, board design, board bring up and system level debugging
- Strong Lab and debugging skills for board and system hardware bring up, testing and characterization
- Experienced with leading pcb physical design, and design for EMC, test and manufacturing.
- Leadership in taking ownership of system design and driving the cross-functional team in delivering a high quality product with a fast paced cadence.
- Familiar in collaborating with contract manufacturers
- Minimum of 5 years of relevant industry experience is desired
- Expertise in board CAD tools including Cadence Concept and Allegro
- Expertise in Unix and automation scripts
- Excellent written and verbal communication skills

Description

CPU, Graphics, Power, Analog Circuit, Audio, Wireless and high speed I/O integration
Collaboration with cross-functional design teams on system integration and release of the design to high volume manufacturing with high quality and good design margin.

Education

BSEE required, MS or Ph.D. preferred

Hardware Systems Integration Engineer

Job Summary

The iPhone Division is seeking enthusiastic electrical engineer for the iPhone Engineering Team. This is a design engineering position that entails working with cross-functional groups to continually push the envelope of technology implemented in iPhone products.

Key Qualifications

- The ideal candidate is someone from a consumer electronics background who has designed high volume, low power, high quality products.
- BS/MS EE
- Must have great EE fundamentals
- Must have hands-on experience in prototype bring-up and debugging, functional verification and manufacturing support
- Must have experience designing high volume products from beginning to end and dealing with contract manufacturers
- Broad experience in small embedded systems, especially 8-bit and 16-bit micro-controllers
- Experience in the following areas is important: system integration, processor selection, various communication protocols (especially USB, SPI, I2C and UART), analog integration, audio design and measurement, sensor design and integration & low power design
- Experience in the following areas is desirable: Bluetooth product design, video electronics design and measurement.
- Tools: Cadence Concept & Allegro
- Position will require some international travel.

Description

Responsibilities include: Schematic capture Manage PCB layout design Prototype bring up and debugging Functional verification Signal characterization Working with and managing suppliers/manufacturing partners Hardware bug tracking

Education

BS/MS EE

Hardware Validation Engineer

Position Responsibilities:

- Help create specifications, design schemes, project functional blocks, correctly select parts and vendors, test equipment for new projects and modify/enhance existing designs
- Responsible for schematics, layout and BOM checks, debugging and testing of server boards/add-on cards/backplane products
- Collaborate with validation engineers and LABs to discover and solve any quality related issues of designs in early stages
- Create detailed technical documentation including product specifications, layout instructions, test procedures, etc.
- Resolve complicated issues and work on critical engineering problems
- Ensure product conforms to engineering, regulatory, and customer specifications
- Transfer products to manufacturing with concise documentation, and help CM and technical support solve problems
- Perform other duties as assigned

Position Requirements:

- BSEE or MSEE, plus 1-3 years experience
- Wide knowledge of PCB layout, Allegro tools, circuit design, use of debugging tools, oscilloscopes, logic analyzers, 12C bus analyzers, etc.
- Knowledge of high speed circuit design, and voltage regular designs
- DC to DC switching circuit design related experience
- Strong communication, written, oral, organizational, and interpersonal skills
- Self-motivated team player with ability to work in a fast-paced environment

IC Design Verification Intern

Business Unit	Mobile and Wireless Group
Job Description	Looking for an intern to help with various Lab bringup and simulation/IC design activities. The tasks include but not limited to the following: <ul style="list-style-type: none">- RTL Simulation and verification of existing products- Support lab bringup of Silicon- Electrical characterization of new products once the first silicon is proven
Job Requirements	<ul style="list-style-type: none">• Currently pursuing MS or PhD in Electrical Engineering• Experience with C, TCL and Python scripting languages• Experience with build environments for large projects, a plus• Experience in lab using standard equipment• Experience in lab working on embedded systems• Interact with engineers in various groups to accomplish tasks
Country	United States
State/Province	California
City/Town	Sunnyvale
Shift	1st Shift - Day
Percent of Travel Required	None
Function	Engineering
Discipline	Intern

Kernel Engineer

Posting Title

New College Graduate- MTS- VM Core Kernel

Job Description

VMKernel Hardware team is part of the Core Platforms Group responsible for ESX Operating System and Core Virtualization Infrastructure. Engineers interested in systems architecture, systems software and distributed systems get to design and implement novel operating systems techniques and algorithms for managing virtual and real computer hardware. They work on harnessing the power of highly sophisticated computer and storage hardware.

Performance Objectives:

- Design and implement high quality OS components of an enterprise-class virtualization platform
- Work closely with others within Engineering and other VMware organizations
- Design and develop new features and enhancements to existing capabilities
- Analyze and debug user and kernel space issues

Competencies:

- MS or PhD in Computer Science, Computer or Electrical Engineering or equivalent
- A passion for writing high-quality, well-architected and well-documented code
- Strong analytical and debugging skills
- Excellent C programming skills
- Knowledge of x86 assembler, C++, Java, Perl, Python and other programming languages a plus
- Good verbal and written communication skills
- Knowledge of one or more computer hardware architectures
- Good understanding of modern Intel and AMD x86-based processor architectures a plus
- Excellent understanding of Operating Systems concepts and kernel components
- Experience with SMP, memory and CPU management, multi-tasking, device drivers a plus
- Familiarity with one or more virtualization platforms a plus
- Experience with low-level / kernel-level code development
- Experience in developing modular and extensible code
- Familiarity with kernel performance analysis and optimization

Linux & Networking Engineer

Overview of Responsibilities

Use your technical expertise and knowledge of embedded technology to assist account teams in closing large opportunities. In addition, you will be responsible to provide professional engineering services and customer trainings that enhance customer success while using Wind River products. Along the way, product deficiencies and enhancements are communicated to product engineering groups at Wind River. It is critical that the individual in this role is constantly developing and deepening his/her expertise in the latest products and industry technologies.

Detailed Responsibilities

- Assist Account Teams in defining technical solutions.
- Architect, develop, and deliver Proof of Concepts, demos and presentations.
- Technically support customers with product Use Case evaluations.
- Deliver consulting, training, mentoring and debug assistance services.
- Develop technical content and use cases for on line support, tech pubs, and customer trainings.
- Customer Support escalation handling
- Provide meaningful, customer-centric feedback to product engineering via formal and informal channels.

Seek out product, design services and customer education opportunities that will enhance customer success, generate revenue, and reduce the overall support burden.

Requirements

- BSCS/BSEE Degree, MS preferred, plus five years of experience minimum
- US citizen
- Wind River products, embedded computing, and Network industry expertise
- Networking protocols and Linux
- Low level device driver, BSP, and OS development experience a plus
- Strong technical problem solving skills
- Excellent interpersonal and communication skills
- Ability to travel to customer sites

Media Engineer

Essential Duties & Responsibilities

- Create new XML job feeds for clients, designing and successfully distributing job content to media vendor platforms. This includes correctly mapping client job data into the XML formatting.
- Maintain and manage existing job feeds, promptly making changes to XML feeds to meet client requirements.
- Execute project work plan within scope and revise as appropriate to meet clients changing needs and requirements
- Document tasks and milestones in project management system.
- Support of all SuccessFactors processes and policies across organization
- Search Engine Optimization (SEO) Site-Map and distribution management

Knowledge & Experience Requirements

- Education: Bachelor's degree in a technology focused degree or another rigorous discipline.
- Three or more years' related professional experience, ideally in a SaaS / e-commerce & application processing environment.
- Experience with web crawling, indexing, and data-collection technologies desired
- Experience with XML language, indexing and feed technologies desired.
- Experience with SQL and/or other database languages
- Experience in a technical support role (both vendor and client facing)
- Two or more years' experience in a start-up or highly entrepreneurial environment.

Personal Attributes & Required Skills

- Strong work ethic, highly motivated and self-directed.
- Strong analytical skills required.
- Effective written and oral communication skills as well as strong interpersonal skills.
- Attention to detail with a technical background.
- Ability to multi-task and manage multiple projects in a fast paced environment.
- Ability to solve problems and think creatively about process improvements
- Ability to work with internal partners and external clients.
- Ability to explain XML and distribution processes and procedures to non-technical people.
- Shared excitement to be in a fast growth company

Software Experience:

- Competent in the use and knowledge of databases, SQL, JavaScript, HTML, XML, CSS and AJAX technologies.
- Experience in scrape / web crawling technology (e.g. Kapow, Apache)
- Experience in ad serving technology (e.g. DART, Adwords)

MEMORY VALIDATION ENGINEER

Working in NVIDIA's Mobile Hardware System Engineering group, you will be responsible for bring up, validation & debug of future Tegra products. Work as part of a team with logic, circuit design, architects & PCB engineers to ensure successful product development with aggressive product cycles.

RESPONSIBILITIES:

- Perform DDR2/LPDDR2/DDR3L bring-up and validation on Tegra processors
- Perform signal measurements and protocol checking on DRAM interface to make sure designs are meeting JEDEC Spec
- Optimize memory register settings for system stability, performance and power on Tegra processors
- Perform regression and interoperability testing on DDR2/LPDDR2/DDR3L and all future DRAM technology with Tegra processors across process, voltage and temperatures
- Support DRAM vendors in DRAM validation/certification
- Debug ASIC and board issues related to logic design & signal integrity
- Work closely with Application Engineering teams to resolve customer issues in time critical environment

MINIMUM REQUIREMENTS:

- BS or MS degree in EE/CE
- Good understanding on DDR2/LPDDR2/DDR3 memory signaling/protocol
- Good knowledge on PCB design and high speed signal integrity issues
- Well versed in the usage of electrical measurement tools such as oscilloscopes and logic analyzer
- Rudimentary scripting skills (Python & JavaScript).
- Detail oriented, reliable, Self-motivated and able to work under pressure

Mixed Signal Design Engineer

Description

Job Description: Designs, develops, modifies and evaluates complex analog and mixed signal electronic parts, components or integrated circuitry for analog and mixed signal electronic equipment and other hardware systems. Determines creative design approaches and parameters. Analyzes equipment to establish operating data. Conducts experimental tests and evaluates results. Applies and uses independent evaluation to select components and equipment based on analysis of specifications and reliability. Evaluates practical capability of vendor to support product development. Also includes Analog, Mixed-Signal and RF Development Engineers with specialized skills and expertise in developing Monolithic Integrated Circuits for wireless and wireline communications systems and products using CMOS, BiCMOS, SiGe, GaAs Process technologies.

Qualifications

You must possess a Master's degree in Electrical Engineering, Computer Engineering or a related discipline and have graduated within 18 months.

Additional Qualifications:

- Strong academic background required in CMOS semiconductor device physics and silicon processing.
- Relevant coursework in CMOS digital, analog, and I/O circuit design
- Knowledge of transistor-level circuit simulation tools such as SPICE, Verilog, etc.

The following preferred qualifications would be an added advantage:

- Familiarity with CMOS transistor and semiconductor device layout methods
- Experience using custom design environments such as Cadence Virtuoso
- Knowledge of DRC, LVS, and post-layout extraction tools
- Proficiency in UNIX and strong programming skills using C++, Perl, TCL, etc.
- Project work using device modeling tools such as hsim, Ultrasim, etc

MOBILE APPLICATIONS ENGINEER

RESPONSIBILITIES:

- The candidate will work in a team of application engineers supporting multiple customer programs
 - * Creation of Customer Requirements Document
 - * Schematic Review
 - * Board layout Review
 - * Power Tree definition & Analysis, incorporating a preliminary power estimation
 - * Boot Configuration Table definition
- To feedback issues and work closely with core engineering teams to solve customer system issues
- To provide the technical content for RFI/RFQs
- Development and provision of technical training
- Development of technical support materials including customer specific documentation
- To diagnose problems with customers designs and troubleshoot customer issues
 - * Initial Power On sequence validation
 - * Memory Characterization
 - * LCD & Camera Panel
 - * Platform Power Optimization
 - * Audio Tuning
 - * RF Testing
 - * Carrier Qualification
- To provide high quality technical support to strategically important customers
- To liaise with the customer through email, conference calls and visits
- Taking an active role in assessing the technical details of new customer projects
- To represent the customer from a technical viewpoint facing internal development teams
- Chip bring Up & Failure Analysis Support

MINIMUM REQUIREMENTS:

- degree in Electronic Engineering
- 2+ years of experience in mobile phone or tablet hardware design and system architecture
 - * Wireless 802.11xx, 3G or LTE/4G designs
- Excellent diagnostic and analytical skills
- Good documentation skills
- Experience in C/C++ code programming
- Knowledge of HW test and manufacturing processes
- The ability to understand customer business and technical needs and to translate these into a solution description/spec for NVIDIA core engineering teams.
- Customer support experience

Network Engineer

A Verizon role is a chance to channel your drive and enthusiasm. An experience that puts you front and center. Hands-on. Contributing your creative energy to high-impact projects from the moment you arrive. And because we know the value of your education, we pay you for the talent and solutions you bring to the table. Our roles will offer you the opportunity to be involved in meaningful work/projects; be exposed to daily operations; attend informational sessions; participate in networking activities with other interns, professionals, and business leaders. You'll learn from the best minds in the industry and have a unique opportunity to use your engineering knowledge and skills in an environment dedicated to maintaining industry superiority. You will be introduced to useful tools and resources that will help you grow both professionally and personally throughout the program. Internship opportunities are available in areas including: Wireless Device Testing Network Operations (NOC/NRB) Website Design Desktop Support Application Testing Software development Network Design and Optimization Technical Operations Product Development Candidates graduating after December 2013 are encouraged to apply. Start dates for these internship opportunities are typically in the spring/summer.

Qualifications

To be successful you'll need: To be enrolled in a Bachelors/Master's program for Electrical Engineering, Computer Engineering, or equivalent major To be graduating after December 2013 3.0 GPA or greater in Major preferred Understanding of RF & IP technologies Strong analytical skills and solid communication skills Strong computer skills in MS Office (especially Excel) Enthusiasm and eagerness to learn Our recruiters will review your application for available Engineering internship opportunities at Verizon. If a possible match is identified, a recruiter will contact you to further discuss your skills and professional interests.

Network Engineer with LTE Experience

We have **Network Engineer with LTE Experience Position**. This is a Fulltime (long term) position. Selected candidates must be willing to join at a short notice.

The ideal candidates will possess the following:

- Knowledge of VoLTE (Voice over Long Term Evolution)
- Working with business teams to plan and execute UAT
- Ability to work with non-technical staff, Telecom/Communications skills/background, and to work independently,
- SIP, VOIP, IMS experience
- Strong understanding of protocols: SIP, DNS, ENUM, diameter, Sig Tran, etc
- KPI verification: CPU, re-transmission rates, etc
- Ability to troubleshoot issues and analyze detailed technical information as well as being able to roll up the details and provide client management a summary level status
- Network element performance testing and test case creation and execution
- Ability to work on and track multiple projects simultaneously
- Provide input to help resolve design issues and develop strategies to make ongoing improvements that support necessary performance
- Resolve and work on issues across multiple elements and protocols
- Highly organized and process oriented
- Telecommunications call processing experience
- Work directly w/client/status reporting
- Manage issues.
- Strong written, verbal and oral presentation skills
- MS Office tools (Visio)

NETWORK ENGINEER

Position Description

Lenovo is not just another technology company. We make the technology that powers the world's best ideas. We design tools for those who are driven by accomplishment. We are the company that powers the people who Do. The engine that helps them Do more. Do better. Do what's never been done. And we are united in the quest to help our users defy the impossible.

Position Requirements

- BS degree minimum in EE, Computer Engineering or Computer Science
- Proficient in written and spoken English to explain technical problems on the phone and emails with US based supplies
- Responsible for creating execution plans for the Network components in the server (NICS, and CNAs) for the supported platforms based on the direction established by the Architecture team
- Experience with various Network topologies, NICs and CAN HBAs, Firewalls, proxies, routes and switches
- Familiarity with systems, computer architecture and PC/Server interfaces such as iSCSI, NFS, CIFS, HTTP, FTP
- Knowledge of Thin Client, TCP/IP, IPSEC, SSL, RDP and VPN protocols
- Ability to configure and install a server with various NICs and HBA Cards in the system in various operating systems, including Windows Server editions, Linux or Solaris operating systems.
- Ability to provide Linux kernel, driver and application analysis and support
- Ability to accurately and consistently follow instructions delivered in an oral, written or diagram format while also providing feedback with a goal of constant improvement.
- Experience with installing, configuring and maintaining Servers with various NICs and able to troubleshoot issues
- Experience capturing network traces
- Knowledge of operating systems & drivers and how they interact, including NIC teaming
- Experience troubleshooting issues and able to work with the vendor FAE (Field engineer) to provide issue details and find root cause resolution to the issue at hand
- Ability to multi-task, managing multiple assignments concurrently; quickly and accurately shifting attention across tasks within distracting conditions without losing focus on quality

Desired Skills and Experience:

- MCSE certification or equivalent experience.
- Familiarity with WMI
- Experience with software for server manageability infrastructure based on IPMI/DCMI and other standards
- Experience with market leader IP switches (such as Cisco, Force10, Brocade)
- Experience with iSCSI products such as EMC VNXe, EqualLogic or Compellent products
- Knowledge of hardware and/or software debugging tools, protocol & logic analyzers, and performance benchmarking is a major plus.
- Experience with Linux kernel level drivers
- Experience with benchmark tools and automation tools

Optical Design Engineer

Staff Engineer Optical Design-03807

Description

- Candidate will own the Success & Failure Analysis, and Test Site design, for the HAMR head and will work in a team environment to deliver product to performance, cost, yield, and time-line targets. The Engineer will interface with the modeling (optical/magnetic/thermal/recording), process development, characterization, backend, and product teams to understand the performance and determine factors we must refine to realize product roadmap.
- Expected to perform at a high-level of technical competence, with sufficient experience and expertise to successfully complete complex tasks consistent with meeting product objectives. Able to systematically work through cause-effect analysis, distill learning from data, and drive experimental and modeling plans to expose learning. Considered a subject matter expert capable of identifying, explaining, and applying cause-effect relationships which are important for decision making.
- Owning HAMR SA/FA & Test Site Development includes:
 - Design test structures (optical, thermal, magnetic) and methods to characterize head function and yield.
 - Analysis of factors (head optical, thermal, magnetic) to determine how well they meet or miss intended recording performance;
 - Working closely with Head Optical & Magnetic designers, and Process teams, to understand performance and limitations;
- Defining design verification testing, and work closely with validation and metrology teams to run test plans and analyze data to understand performance to budgets

Qualifications

- MS required / Ph. D. preferred in Physics, Electrical Engineering, Material Science or a related field.
- 2+ years experience in design of experiment, magnetic/optical performance characterization and problem solving. Knowledge of magnetic recording and test. Optical and/or MEMS device experience is a plus. Have a good understanding of recording physics, integrated optical and magnetic component design and test.
- Extensive experience understanding how things work, and why they don't.
- Proficiency with Matlab and JMP is required.
- Experience with statistical data analysis and Design of Experiment (DOE) is required.
- Experience with FMEA is required.
- Experience modeling (FDTD, FEA, FEM, umag) a plus.
- Excellent verbal and written communication skills.

PCB Layout Engineer

Scope of Duties / Job Description:

Combine mechanical, schematic, net list and guidelines as input to develop PCB state-of-the-art layouts for solid-state disk drives
Complete PCB layouts on schedule without compromising quality standards
Ensure PCBs are designed, produced and documented in line with established methodology, standards and procedures.
Create and manage component libraries, including PCB footprint libraries
Design rule verification
Create full documentation package for PCB fabrication and for PCB assembly
Generate Manufacturing file
Abide by established procedures and best practices for developing high quality PCB designs and products which conform to customer and company quality standards.
Collaborate with other engineers on the design team to achieve optimum performance and meet project development schedules and cost targets
Recommend engineering projects to help the organization in maintaining and improving its competitive position.
· Identify and recommend changes to improve productivity and reduce cost and scrap.
Work effectively with team members, exhibit a professional manner in dealing with others, and maintain constructive working relationships.
Keep current on information and technology affecting functional areas to increase innovation and ensure quality
Perform miscellaneous duties and projects as assigned and required.

Key Competencies / Qualifications:

Familiarity with Cadence Allegro.
Familiarity with IPC and IPC guidelines
General understanding of electronics and components
Ability to read and understand schematics
Schematic Capture and PCB layout
Appreciation of EMC – Understanding of EMC and design considerations
· Design for manufacturability
Test procedures
Liaison with subcontractors (layout, PCB manufacturers, Assembly)
Enjoys working in a fast-paced, results-oriented organization
Design documentation, Revision Control, Configuration Management
Problem analysis and problem resolution at both a strategic and function level
Self motivated with the ability to be adaptable – manages competing priorities and is able to deal with unexpected events
Crisp decision making skills
Works well with hardware engineering team and is capable of carrying out needed engineering initiatives
Professional demeanor and team player
Demonstrated critical thinking, analytical skills and problem solving

Strong, positive interpersonal skills

PHYSICAL OPTIMIZATION INTERN

Position Description

As an intern of our optimization team, you will perform research and development on cutting edge timing optimization issues. You will be working on the forefront of technology in the areas of timing/area/power and Signal Integrity optimization.

Position Requirements

Qualifications:

- Understanding of Core EDA algorithms in timing analysis/optimization• Excellent C++ programming skills
- Experience with Multithreaded programming and debugging is a plus
- Good written and oral communication skills
- Team player, with a desire to improve overall quality of software

Company Information

Cadence is the global leader in software, hardware, and services that is driving the transformation of the electronic design automation (EDA) industry. This application-driven approach for creating, integrating, and optimizing designs helps customers realize Analog & Digital ICs , System-On-Chip devices, IP and complete systems at lower costs and with higher quality.

Cadence is an equal opportunity employer and is committed to hiring a diverse workforce.

Job Title: Power Engineer

Responsibilities:

- Designing, developing, and qualifying BMP Systems to meet the power requirements of new products
- Designing and developing BMP power solutions with high efficiency and power-density that are reliable, flexible and cost-effective
- Generating power module specifications and working with suppliers to develop these custom power modules
- Working on team initiatives including developing and improving team and cross-functional processes
- Defining, directing, and participating in power design verification tests
- Ensuring that all hardware engineering projects, initiatives, and processes are in conformance with organization's established policies and objectives
- Assisting in the resolution of power related field / product issues for assigned projects

Requirements:

The candidate will have completed relevant analog or power course-work, and possess the capabilities to quickly learn the following skills:

- Power conversion topologies and tradeoffs, analog circuit design, and circuit simulation
- Power system loop stability analysis and optimization
- Power Integrity analysis and tools
- Prototype bring-up and troubleshooting of BMP systems
- Digital power management and related programming
- Schematic capture and CAD layout review tools
- Stability analyzers, oscilloscopes, function-generators, electronic loads, etc.
- Ability to work in a fast-paced environment, successfully handling several projects and initiatives simultaneously
- Ability to think and act pro-actively and drive issues to resolution
- Ability to work effectively with cross-functional teams
- Excellent verbal and written communication skills
- Excellent presentation skills, with focus on presenting to various audiences effectively
- Ability to quickly document the results of your work in a clear, concise, and complete manner

Education

- This position requires a BSEE

Power Management Engineer

Job Summary

Imagine what you could do here. At Apple, great ideas have a way of becoming great products, services, and customer experiences very quickly. Bring passion and dedication to your job and there's no telling what you could accomplish. The iPhone and iPod touch software team is looking for a QA engineer to help us ensure all of our products have best-of-class battery life.

Key Qualifications

- Excellent understanding and application of a scripting language such as Ruby or Bash
- Some experience with test automation.
- QA experience or exposure to good testing methodologies preferred.
- Excellent organizational skills.
- Excellent communication skills, both oral and written.
- Excellent understanding of triaging problems.
- Some exposure to hardware, from home electronics projects to building a PC from parts.

Description

Responsibilities include running tests of both projected and actual battery life across a large amount of hardware, triaging reports of battery life issues, and helping us find areas where we can improve battery life. Successful candidates will have excellent organizational skills -- you'll be running many different tests at a time across many different scenarios and need to keep careful track of the results -- as well as the creativity and initiative to help us proactively discover battery life issues or areas for improvement.

Education

BSEE/BSCS

Power Management Elec Eng

Scope of Responsibility/Expectation:

Join the System Power Management team, working with interdisciplinary teams comprised of Mechanical, Electrical, Software, and Design engineers to develop, integrate and optimize circuits & systems into high volume mobile products that are sold worldwide. Produce innovative designs with consumer appeal to meet size, cost, time to market, and quality targets.

Specific Knowledge/Skills:

- Minimum 4 year degree in Electrical Engineering or equivalent.
- 4 years experience with concentration in Power Management, Digital and Analog Design, Application Processor/Peripheral Interfaces and Baseband Systems.
- Self motivated and passionate about designing world-class consumer electronics
- Strong problem solving skills
- Good sense of time management
- Good written and oral communication skills

Candidate should possess the following knowledge/skills:

- Design Knowledge of Power Management systems, voltage and frequency scaling for optimal power usage, switching regulators.

Candidate should possess some or all of the following knowledge/skills:

- Design Knowledge of Application Processor Systems, including interfaces such as internal and external memory, Bluetooth, WLAN, NFC, Imagers, LCDs, Gyroscopes, Accelerometers
- Create supporting technical documents and test procedures. Circuit simulation capability using SPICE or similar methods. Automation tools for data collection and analysis such as Labview, Visual Basic, C , Perl, Python, etc. PCB design tools such as Allegro.

QA Software Engineer

Position Summary

As a WCMS QA Engineer, you will be responsible for testing, coordinating, and providing project testing status on Adobe.com WCMS projects which are developed on Adobe Experience Manager/ CQ platform. You will work closely with SCRUM teams members, QA lead/manager, and involved offshore teams to ensure the successful release of all scheduled milestones. This is an excellent opportunity for a highly motivated individual to gain exposure to Adobe.com web projects developed on Adobe Experience Manager/ CQ platform and various Internet technologies.

Requirements

- Solid knowledge and interpretation level skill are required for the following:
- Strong test experiences on Template and Component from both Authoring and Publish perspectives.
- Hands-on test experiences on Web projects that are leveraged from AEM/CQ features such as Digital Assets Management, Social Collaboration tools, etc.
- Solid interpretation skill in HTML/DHTML, XML, JavaScript, CSS, or Java is a plus
- Solid understanding in Web Content Management System infrastructure, Dispatcher, Sling or OSGi frameworks, CDNs, etc.
- Experienced working in a SCRUM team and good understanding of agile process.
- Work closely with QA lead/manager, project manager and SCRUM team members in a fast paced environment, you would be required to provide real time status update and testing support.
- Experience and ability to quickly build rapport, and to communicate technically and efficiently with team members and project owners in both US and offshore. Excellent written and verbal communication skills in English are strongly required.
- Experience and ability to assist QA lead/manager with offshore QA project status communication and to ensure quality objectives and milestones are met.
- Experience and ability to create QA documentation such as test plans, QA project reports in formats like Wiki, MS excel, MS word, MS project, HTML.
- Ability to adjust and respond to scheduling and/or technical changes, with the ability to analyze and communicate any increased risks and/or testing considerations resulting from such changes to the QA lead/manager.
- Solid familiarity with modern operating systems and browser technologies (and their known compatibility limitations) and mobile devices.
- Ability to set-up and configure hardware systems for testing purposes, such as virtual machine technology (VMWare).
- Ability to ensure various operating system configurations are available and tested by local QA team.
- Experience and familiarity with QA's role in the web software development and SCRUM Agile process.
- Experience and familiarity with at least two commercially available defect-tracking systems (Bugzilla, JIRA, Watson, ALM, etc).
- Experience with automation tool such as Mercury QTP and Xenu Link Sleuth is a plus.
- Four-year College or technical degree in Computer Science field or equivalent work experience.

Job Title	RF Hardware Engineers
Location	Colorado - Boulder
Job Function	Qualcomm CDMA Technologies (QCT) is looking for RF Hardware engineers who will become members of our core hardware development team. The roles and responsibilities encompass the incorporation of Qualcomm ICs (RF and baseband) into demonstrator handsets and reference designs. Design activities will include preparation and participation in design reviews, component selection, schematic creation, layout optimization, hardware evaluation and optimization, and data collection.
Responsibilities	As part of the design and design qualification responsibility, the RF Hardware engineers are responsible for providing regular project status updates and working closely with RF leads, baseband and support engineers in a hands-on environment. Frequent interaction with design engineers from various QCT groups will be required including those from customer engineering, RF systems, test engineering, digital/analog chip design, software, mechanical, PWB design, and manufacturing.
Skills/Experience	The RF Hardware Engineers being sought are recent college graduates with academic and co-op or intern hardware design and design qualification experience. Candidates must have familiarity of wireless standards and a background in the design of wireless products and circuitry including CDMA, WCDMA, GSM, and LTE receivers, transmitters, and synthesizers. Knowledge of GPS Receivers and Bluetooth transceivers considered a plus. Ideal candidate would also have experience in RF layout techniques using multi-layered FR4 PWB material. Experience in a Product Development environment is a plus, this requires an affinity for lab work. Comprehensive knowledge of the set-up and operation of complex RF test equipment required for hardware validation is preferred. Must be a self-starter who is capable of working independently and as part of a team. Strong written and verbal communication skills are required, including the ability to produce and present technical and project information to engineering management.
Education Requirements	Required: Master's, Electrical Engineering

RTL Design Engineer

Educational Requirements:

- MS degree in Electrical or Computer Systems Engineering or Computer Science + 3 years practical experience in logic design

or:

- BS degree in Electrical or Computer Systems Engineering or Computer Science + 6 years practical experience in logic design.

Job Function:

- SOC design team is seeking experienced ASIC/RTL Design Engineers with complex SOC design and integration expertise for our Boxborough site.

Responsibilities:

- Successful candidates will be responsible for leading, and participating in, the design of leading edge SoC's in advanced digital CMOS processes. Our RTL Design Engineers are expected contribute in all aspects of SoC design including:

- Chip definition
- Architecture development and modeling
- Development of micro-architectural specifications
- Conversion of micro-architectural specifications to logic implementation
- Verification, emulation, debug, synthesis, and timing closure
- Interfacing with physical execution, software, and silicon bring-up teams

Experience required:

- SoC Architecture; knowledge and hand-on experience from industry ASIC design flow including RTL coding, debugging/verification, and supporting synthesis and timing closure

RTL Design Engineer

Description:

We are seeking an RTL Engineer for our hardware development team. In this role, you will be responsible for design, verification, and support efforts to enhance our NTO product line and IP development. You will have the opportunity to work on cutting edge projects for FPGA and network processes and emerging technology such as 40G and 100G Ethernet. This position is based in Austin and will report to the Senior Manager of Hardware Development.

Responsibilities:

- Accurately scope moderate to large features
- Able to identify factors affecting a potential implementation
- Able to design and document a feature of complex scope
- RTL Design targeting FPGA (Altera/Xilinx)
- Design Data path and Memory elements at high speed and multiple clock environments
- experience with Backend FPGA flow like synthesis, place route, timing analysis
- Experience using third party IP like external memory interfaces such as DDR2/3, Ethernet PCS, and MAC desired
- Knowledge of scripting languages such as Perl/Tcl/Make is desired
- Good debugging skills, signaltap, oscilloscope, logic analyzer
- Must be proficient in Verilog

Requirements:

- Masters of Science degree in Electrical Engineering with a minimum of 4 years of experience
- Bachelor of Science degree in Electrical Engineering with at least 6 years of experience
- Experience must be in working with industry standard interconnect protocols, networking, and system applications.
- Proficiency in these tools: Verilog, Altera Quartus, C, TCL, PERL
- Excellent verbal and written English communication skills
- Self-motivated and driven to achieve deadlines; highly enthusiastic team player with strong analytical skills

SOC VALIDATION ENGINEER

GPU / SOC SILICON VALIDATION ENGINEER #1543970

NVIDIAs silicon validation engineers are responsible for designing GPUs and SoCs into cutting edge desktop, notebook, professional, and mobile solutions.

- Analyze pre-production silicon for performance, power, yield, and quality to define the worlds fastest power-sipping graphics products.
- Architect essential next-generation product features and work in cross-functional teams to drive them to production.
- Find innovative solutions to complex silicon and system level problems in order to enable product shipment.
- Design tools and scripts to automate data collection, test case execution, and results analysis.
- Ensure compliance with tomorrows high speed display, interconnect, and storage standards.
- Work alongside system architects, chip and board designers, software/firmware engineers, HW/SW applications engineering, product managers, sales, and operations, in a fast-paced, high-energy, collaborative work environment to bring industry-defining products to market.

MINIMUM REQUIREMENTS:

- Minimum GPA: 3.5
- BS or MS in EE, CE, CS, Systems Engineering and 1-3 years of experience in a hardware engineering position.
- Excellent problem solving, teamwork, and communication skills.
- Knowledgeable in MOSFETs, digital design, scripting, signal integrity, statistics, timing analysis, fault analysis, sampling, computer architecture, filters.
- Experienced with lab tools (oscilloscopes, multimeters, logic analyzers).
- Experienced with Perl, C/C++, Windows, and Linux.
- Exposure to BIOS, drivers, and other software applications.

Software Engineer

Job Summary

Under general supervision, analyze, design, develop, debug, and modify software enhancements and/or new products for internal or customer applications.

Using current programming language and technologies, write code, complete programming, and perform testing and debugging of applications.

Complete documentation and procedures for installation and maintenance.

May interact with users to define system requirements and/or necessary modifications.

Perform basic software configuration management tool administration and release coordination processes and procedures.

Ensure software created and released conforms to quality requirements by accurately writing and executing test plans.

- Software design and review
- Software development of application or system components
- Requirements definition and analysis
- Unit testing of developed code
- Investigation and root cause analysis of software and system defects
- Run tools and processes that support and automate software product release
- Generate, monitor and maintain Baseline Audit, Configuration Item Version Control, Configuration Status Accounting and Configuration Identification
- Publish release packages and records
- Deploy the development artifacts to a staging area for release to production
- Create and execute approved test plans and analyzes and reports results
- Decompose requirements for testability and provides feedback to the requirement gathering process
- Interact with customers/users to support software acceptance process and acts as a liaison between development and user community

Software Engineer

If you've been following Dropbox for a while, you know that we've always tried to keep it simple -- in reality, the stuff under the hood is pretty complicated. Over the past couple years, we've had a handful of brilliant minds craft the thousands of moving parts that make up Dropbox, while keeping it easy enough for your grandma to use. Over 100 million people around the world now rely on us to save more than 500 million of their files every day.

Here are a few of the technical challenges we've tackled:

- Scaled MySQL to handle metadata for hundreds of billions of files.
- Created cross-platform custom Python memory allocators to keep our client memory footprint low when dealing with lots of files.
- Built a custom LAN protocol allowing Dropbox clients to communicate and transfer files between themselves when on the same local network.
- Reverse engineered operating systems to add icon overlays and contextual menus.
- Designed custom servers that handle millions of concurrent connections.

Dropbox's programming challenges of efficiency and scalability mean there's still a huge range of projects to choose from, and our development cycles will let you see your idea from Monday in millions of peoples' hands on Friday. Meanwhile, you'll be working with talented, like-minded teammates to bring the complex parts of Dropbox together into something that simplifies peoples' lives.

**Job Posting
Title**

Software Engineer

As a Software Engineer, you'll become a key contributor where your skills and input make a big difference. In this role, your responsibilities will include:

- Design and development of embedded software solutions that enable TI devices in a broad range of customer applications.
- Provide software support for TI's next generation of processors.
- Work closely with business partners and customers, as well as TI's marketing, systems and applications engineering teams, to collaborate and solve business problems.
- Apply good programming skills and judgment to ensure software designs that maximize software reusability across various processors.

We're searching for a student who is pursuing an undergraduate or graduate degree in Electrical and Computer Engineering, Computer Engineering, Computer Science, Electrical Engineering or a related field. A minimum cumulative 3.0/4.0 GPA or higher is needed.

Embedded C and C++ programming skills and familiarity with assembly language programming are also needed. Knowledge of software engineering processes and the full software development lifecycle is desired.

Excellent writing and communication skills are a must. Strong time management skills and the ability to work to a deadline are very important.

Texas Instruments is an equal opportunity employer and supports a diverse, inclusive work environment.

**Job Opening
Type**

New College Grad - Non Co-Op

**Minimum
Education Level**

Bachelor's Level Degree

**Related Field(s)
of Study**

Computer Engineering
Computer Science
Electrical Engineer

Software Engineering Intern

Position Description

- Working closely with VIP CTO
- Research on abstract debug (software, RTL)
- Prototyping debug solution (design and programming)
- Need understanding data structure and algorithm design.
- Need fluent with C/C++ programming
- Need excellent communication skills

Position Requirements

- Software Engineering
- 2 months in the summer
- Majoring in Math, CS, or EE.
- Reporting to VIP CTO
- Physically in SJ during internship period
- Need f2f interview in SJ
- Area of research and development: research on abstract debug and prototyping development.

Company Information

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Cadence is an equal opportunity employer and is committed to hiring a diverse workforce.

Software Security Engineer

Come help Dropbox build the world's most secure file storage platform that's still easy enough for your grandma to use! Over 100 million people around the world now rely on us to save more than 500 million of their files every day.

Responsibilities:

- Design and build new frameworks, infrastructure, and custom internal tools to improve product security
- Work on new product features to make Dropbox users and data more secure
- Advise engineers and teams on security and privacy issues; you'll be involved with every aspect of Dropbox engineering!

Requirements:

- Independent and comfortable working in a fast-paced environment
- Excellent communication skills, on both technical and non-technical issues
- Passion for security, and a practical and balanced approach to security issues; knowledge and experience recommended
- And of course, you'll still be a software engineer -- so strong software engineering skills

SOFTWARE TOOLS ENGINEER

RESPONSIBILITIES:

- Develop, code, qualify, and maintain Tegra system software automation tools
- Provide robust solutions that meet/exceed aggressive timelines
- Collaborate with fellow software engineers on larger projects

MINIMUM REQUIREMENTS:

- Solid background of computer architecture and operating system fundamentals
- Strong C#, C++, and C programming skills are required
- Understanding of Java, JavaScript, Perl, Python and WPF programming languages is a plus
- Experience with Android, Windows Mobile, and Linux operating systems is a plus
- Experience designing, coding, and debugging large and complex software modules
- Strong ability to multi-task and manage multiple assignments
- Excellent communication skills, good team player and the ability to take initiative
- BS or MS degree in Computer Engineering, Computer Science or Electrical Engineering
- 2+ yrs of relevant industry experience

SSV Timing and Power Sign-off Intern

Position Description

Encounter Timing and *Power Systems* are leading products for timing/SI and power analysis and optimization of digital circuits. These products offer best usability with full design platform integration and industry's best performance and functionality with leading edge core technology engines. We are seeking an enthusiastic and domain expert software engineer to join as an intern, with a specific focus on graph partitioning techniques. Candidate must have in depth prior experience of working on related EDA areas. Good understanding of timing / power analysis and experience in circuit level concepts, matrix theory for solving large sparse linear systems and optimization theory is required. Strong background in timing analysis will be preferred. Development experience in C++ and object oriented programming on unix platform is essential. Good written and oral communications skills are required. Prior experience in physical design, simulation, and/or sign-off verification is desirable. Knowledge of GDSII, LEF/DEF and DSPF/SPEF and SPICE formats will also be helpful.

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STORAGE DEBUG ENGINEER

Position Responsibilities:

- Customer Support for technical issues related to storage server system
- Design test strategies, develop test tools and implement test scenarios for finding root cause of failures
- Perform storage backplane signal integrity measurement
- Monitor backplane quality and work with R&D team for improvement
- Feedback to R&D to define future needs and manage information from HDD suppliers
- Sustaining support for HDDs and SAS/SATA controller firmware

Position Requirements:

- Bachelor degree in Electrical Engineering or Computer Science, or Masters degree; or equivalent experience required
- Strong working knowledge of Fibre Channel, SATA, SATA drive interface
- Strong working knowledge of SCSI protocol and various RAIDs configuration
- Proficient with Unix/Linux and able to develop test scripts
- Proficiency in English, have good communication skills (written & verbal)
- Ability to work in a team environment and cross department development
- Pro active and aggressive attitude to achieve goal

System Design Engineer

Description

Designs, defines and implements complex system requirements for customers and/or prepares studies and analyzes existing systems. Determines system specifications, input/output processes and working parameters for hardware/software compatibility. Coordinates design of subsystems and integration of total system. Identifies, analyzes and resolves program support deficiencies. Develops and recommends corrective actions.

ESSENTIAL DUTIES AND RESPONSIBILITIES

- Design disk drive Printed Circuit Board Assembly (PCBA).
- Help identify and resolve cross-fictional development issues.
- Assist in the definition of drive test limits.
- Recommend improvements to the error recovery and defect detection process.
- Recommend changes to the power management process.
- Assist in the performance of PCBA and systems level Engineering Verification test (EVTs).
- Identify and interpret data collection requirements and provide yield predictions to program management

Qualifications

- Applies fundamental technology principles to specific tasks.
- Recommends solutions for defined processes.
- Follows department/work group processes and may recommend improvements to them.
- Follows technical documentation requirements.
- Has basic knowledge of business environment/ operations.
- Knows individual and work group/project goals and works towards them.
- Works to meet specific customer requirements and develop rapport.
- Learns to use standard and specialized engineering tools and equipment used by the discipline and/or department.
- Effectively presents information, ideas, perspective to peers, team members and managers and respond to questions.
- Speak effectively one-on-one and in small group situations.
- Apply principles of advance calculus, modern algebra, advanced statistical theory, advanced mathematical concepts such as limits, rings, quadratic and differential equations, permutations, proofs of theorems, exponents, and logarithms.
- Work, collaborate and communicate effectively contributing to team's results.
- Develop and maintain good working relationships with internal and external contacts.
- Gives presentations to colleagues and managers.

SYSTEM SOFTWARE ENGINEER

We are hiring engineers to work in our system software engineering teams. The best candidates will have strong C programming skills, a thorough understanding of operating systems and kernel programming, a good understanding of hardware architecture, and excellent communication and planning skills. You will work closely with both hardware engineers and other software engineers to design, develop, and debug many functional aspects of our multimedia accelerator and mobile system-on-chip (SOC) devices. You will also be heavily involved with the early modeling and simulation required to produce our world-class products, and will follow the devices all the way through the development process to the customer phones, tablets, desktops, notebooks, workstations, and gaming console products that are used throughout the world.

MINIMUM REQUIREMENTS:

- BS or MS degree in Electrical Engineering, Computer Science, or related degree
- Excellent C programming skills
- Familiarity with computer system architectures and the hardware/software interface layer models
- Interest in low-level operating system knowledge, specifically memory/resource management, scheduling and process control, and hardware virtualization
- Kernel experience with Linux, Android, Chrome, or Windows systems is helpful
- Experience with complex system-level debugging is invaluable

SYSTEM VALIDATION ENGINEER

Summary:

Working with Hardware Engineers to validate servers and workstation platforms. Discover and resolve functional issues.

Essential Duties and Responsibilities:

- Resolve issues related to product line internally and externally
- Execute test plan and report issues to design team
- Conduct functional and compatibility testing on various operating systems
- System performance assessment and testing

Qualifications:

- BSCS/BSEE plus 1 year relevant experience or MSCS/MSEE
- Ability to work under date-driven schedules
- Knowledge of hardware and software architecture and be able to apply this understanding to resolve issues
- Ability in organizing and managing multiple projects, setting priorities and working independently under aggressive timelines

Physical Requirements:

The physical demands described here are representative of those that must be met by an employee to successfully perform the essential functions of this job:

- Able to sit for long periods of time
- Able to input data into a computer and work with a computer screen for long periods of time
- Occasional bending and standing
- Standing, walking, sitting, talking/listening, crouching or crawling, reaching with hands and arms
- Lift, carry, push and pull in excess of 15 pounds

Work Environment:

The work environment characteristics described here are representative of those an employee encounters while performing the essential functions of this job:

- Office environment shared with other staff members
- May be susceptible to outdoor weather conditions (i.e., cold, heat, rain, and/or wind)

System Engineering

Scope of Responsibility/Expectation:

- 1) Expertise Android Application development and Framework Internals
- 2) Detailed understanding of Android Internals (Memory Management, Process Management etc)
- 3) Understanding of WIFI, Radio (LTE/3G), TCP/IP & Linux Kernel in relation to Android is desired.
- 4) Experience in cloud computing is highly desired.

Lead a feature/experience and work with carriers on all aspects of system design. Provide technical guidance to development engineers etc. in terms of functionality, API and technical suggestions on feature/code development.

Candidate should be innovative and take abstract concepts/vague requirements and technically lead them to realization.

Specific Knowledge/Skills:

Experience in Java, C/C , Data Structures, SQL, UNIX, Object-oriented Analysis & Design, Real-Time Embedded System Programming and/or Design, Computer Networks, TCP/IP J2EE, J2ME, PERL, LINUX, UML, Wireshark.

Knowledge of Android, Mobile Phones, Microprocessor Architectures and Embedded

Software Development will be required to perform the necessary assignments for this job.

Systems Engineer

Job Description:

The Systems Engineering (SE) Associate will assist in the implementation and management of the worldwide SE programs that are designed to improve SE efficiency and effectiveness.

The SE Associate will develop and maintain several programs and initiatives to build strong relationships between the Product Management, Marketing, Engineering and Support organizations, and the SE organization. The SE Associate will receive comprehensive technology and sales training, and will gradually liaise with the sales organization. Ultimately, the SE Associate will transition into a Systems Engineering role, working with clients to resolve their issues, advising them on product and service choices, and update them on their account status.

Qualifications:

Responsibilities

- Develop and maintain a deep technical understanding of LAN and SAN technology, architecture, and Brocade's products and solutions
- Partner with Account Managers to understand our customer's needs and design the LAN and SAN solutions to exceed those needs
- Present Brocade solutions and demonstrations to customers, partners and/or integrators
- Build and execute joint test plans with OEMs, partners, and/or key application vendors
- Drive the implementation and installation of Brocade technology at customers' sites
- Aid in the identification of SE hard and soft skills requirements and gaps through field polls and studies
- Support programs and meeting cadences for SEs and their managers
- Collaborate with corporate teams to maintain a consistent and predictable availability of basic documentation across all segments and platforms and technologies
- Maintain a "Rolodex" of corporate product and technology experts per segment

Qualifications

- Upcoming or recent college or university graduate (within the last 12 to 24 months) with a Bachelors or Masters degree in a related technical field. Relevant disciplines include, but are not limited to Information Technology, Electrical Engineering, Computer Science, Information Systems, Technology Management, or equivalent. Industry certifications are desired
- Prior experience with configuring LAN or SAN equipment is desired. Knowledge of scripting languages such as Perl, Python, Ruby, etc. desired
- Strong reading, writing, and speaking skills. Ability to create and deliver concise and articulate presentations to large and small audiences consisting of technical or non-technical individuals
- Experience with Microsoft Office Suite and third party applications used in creating presentations
- Prior experience with achieving sales objectives desired
- Experience working with competitive personalities a plus
- Ability to work flexible hours. Able to travel locally and internationally – up to 25%.

Bilingual is a plus

Systems Test Engineer

- * Work closely with the systest team to develop and execute test cases.
- * The successful candidate will be expected to produce certain test automation by request.
- * Provide or enhance tools, scripts, and environments to increase productivity.

Requirements:

- * Currently pursuing BS in CS/EE, or higher degree with following courses preferred - data structures, algorithms, operating system fundamentals and computer architecture
- * Knowledge in networking protocols (e.g., BGP, OSPF, IS-IS, MPLS, LDP, RSVP, VPLS, VPN), is highly desirable.
- * Experience with core & edge routers is highly desirable.
- * Proficiency in Perl and UNIX
- * Web and database development/maintenance experiences a plus
- * Good oral and written communication skills

ABOUT JUNIPER NETWORKS

Juniper Networks is in the business of network innovation. From devices to data centers, from consumers to cloud providers, Juniper Networks delivers the software, silicon and systems that transform the experience and economics of networking. Our products and technology run the world's largest and most demanding networks today, enabling service providers, enterprises, and governments to create value and accelerate business success. Everyday our 9,000+ colleagues come together across 46 countries to realize our company vision - Connect Everything, Empower Everyone. We are innovating in ways that empower our customers, our partners and ultimately, everyone, in a connected world. These customers include the top 130 global service providers, 96 of the Fortune 100 and hundreds of public sector organizations.

Technical Support Engineer

Responsibilities

- Respond to customer inquiries, primarily by telephone and e-mail
- Resolve customer technical issues through diligent research, reproduction, and troubleshooting
- Work directly with other technical support staff, quality assurance, engineering, sales, marketing, operations, and administration to resolve problems
- Document all technical inquiries, develop and review content for knowledge base
- Help test alpha and beta products

Requirements:

- Experience in providing Linux/UNIX and Windows technical support to customers
- Excellent interpersonal skills
- Strong in spoken and written English
- Strong Linux or UNIX administration skills & familiarity with the installation and configuration of Linux or UNIX operating systems & setup and operation of TCP/IP networking on Linux or UNIX systems
- Good practical working ability with UNIX utilities, including editors and command shells
- B.S. in Computer Science, Electrical Engineering, Math, or equivalent work experience
- Working knowledge of Microsoft (Windows Server 2003, Windows 2000, Windows NT, Windows 98, 95, and 3.1; MS-DOS), plus x86 platforms, devices, and networking

Highly Preferred Skills

- Some experience with VMware Workstation and / or GSX Server
- UNIX kernel diagnostics/debugging
- Experience in administration of SAN environments
- Technical Certifications (MCSE, CCNA, Linux Certifications etc.)
- Ability to program and debug Perl administration scripts
- Experience in working with development organizations in a new product introductions role
- Experience with operation and administration of Windows NT or Windows 2000 systems
- Experience in administration of complex SAN environment
- Familiarity with HTML authoring and operation of UNIX-based Web servers

Validation Engineer

Job Summary

Imagine what you could do here. At Apple, great ideas have a way of becoming great products, services, and customer experiences very quickly. Bring passion and dedication to your job and there's no telling what you could accomplish.

Key Qualifications

- Strong knowledge of slow speed interfaces.
- Experience with pre and post-silicon validation
- Strong software skills in C/C++ and scripting.
- Embedded software experience.
- Desired: Device driver experience a plus.

Description

System level validation (correctness, performance, and debugging) of I2S?, UART, GPIO, SPI, SDIO, Power management and other interfaces. Develop low-level software to validate functionality, conformance, and performance of SOC functions. Bring-up and debug devices on new hardware platforms. Work with cross functional teams to support product requirements.

Education

BS in CS or EE

Validation Test Engineer

We are looking for a highly self-directed person to assume a technical role working in the research and development phase of bringing our newest products to market. You will learn the latest cutting edge features in our new products by performing various product validation functions. The experience you gain in this role will enable you to move into other functions, such as F/W Development, Systems Engineering, or additional growth opportunities within Product Validation.

Responsibilities include the following:

- Learn and understand a variety of complex technical specifications and apply that knowledge to using and enhancing our test suite and test processes. Identify new testing needs and develop new testing processes and methodologies.
- Specify requirements for test programs to provide the ability to test the newest, cutting edge features being added to our drive products.
- Interface with the drive program teams to communicate schedules, assess readiness, and assist in making risk assessments for OEM Qualification and high volume production.
- Devise strategies for in-depth functionality and compatibility testing of complex drive features.
- Debug issues involving advanced drive features across a wide variety of hardware and software platforms, including PC's, servers, RAID, and various operating systems (Windows and Linux). Work closely with development Engineers to guide issues through to final resolution.
- Become familiar with using Protocol Analyzers to understand and analyze traces for failure investigation.

Requirements:

- Exceptional written and verbal communication skills.
- Ability to read and comprehend complex technical specifications.
- Familiarity with micro-controller architecture, embedded systems desired.
- Strong debugging and troubleshooting skills.
- Basic programming skills (C, C++ Java).
- BS or MS degree in CS, CE or EE.

VLSI Design Engineer

Job Title: Engineer, VLSI Design

Qualifications: The position requires a Master's degree or foreign equivalent in Electrical Engineering, Computer Engineering, or a related technical field. Must have the following skill set (as evidenced by experience or graduate level coursework): digital IC design, ASIC design flows and methodologies, logic design, Verilog or VHDL, Perl, C, synthesis, place-and-route, timing analysis, Synopsys tools, chip tapeout, post-silicon validation, device-level and transistor-level concepts, and microcontroller design. Must pass company technical review

Description: Multiple openings. Perform RTL synthesis for low power SOC designs using Synopsys Design Compiler, standard cell libraries, and internal IPs. Work with RTL Design Engineers to identify potential issues in Verilog or VHDL coding to aid in efficient synthesis and place-and-route. Liaise with backend design teams to execute place-and-route activities, including floor planning, placement, power grid implementation, clock tree synthesis, routing, timing closure, and physical verification. Coordinate IP database, release processes, and quality check, including physical verification, DRC, LVS, ERC, and Antenna. Apply knowledge of ASIC design flows and methodologies to define timing and power constraints for SOCs. Perform signoff static timing analysis using Synopsys Primetime. Utilize Perl and C language scripts to automate flows and facilitate design tasks. Assist with chip tapeout and post-silicon validation of fabricated chip using a chip tester. Communicate effectively with IP teams to obtain timely deliveries in support of timely and successful SOC tapeouts. Job duties require knowledge of device-level and transistor-level concepts and microcontroller design.

**Job Posting
Title**

Wireless Design - RF/Wireless

Business Unit Mobile and Wireless Group

Job Description Join a fast-paced development team working on exciting leading edge products in the wireless personal area networking. These include standalone BT, GPS radios and highly integrated "combo" radio including BT, FM, GPS, WLAN and other new emerging technologies. Broadcom's products are being used in leading edge cellular, pda, mobile and gaming consumer products.

Broadcom has an Intern opportunity available to support system verification including bring-up, optimization, verification, and productization of highly integrated transceivers.

Other responsibilities include:

- Assist engineers by performing verification/characterization tests in lab, and recording results
- Lab equipment setup, bench testing and debug/data collection of Bluetooth/FM/GPS chips to verify and characterize failure modes
- Prototype chip/board screening, data collection and logging

Job

Requirements

MS or PhD student in Electrical Engineering (EE)

- Ability to work a minimum of 20 hours per week during the school year (40 hours during breaks and summer)
- Must be currently enrolled in school
- GPA 3.0 or above
- Understanding of RFIC, RF systems or communication principles
- Lab experience is preferred